XO5085 Series 1x1 inch, 12 Volt, Sinewave, OCXO







Features:

Excellent phase noise performance RoHS Compliant

Applications:

Microwave Radios Base Stations Test and Measurement Equipment Reference timing circuits

Ordering Information XO5085A -XXX S R Product Series Phase Noise Option A: Ultra Low Noise B: Standard Assigned Customer Specific Package Type S: Contact Factory for SMT option (Blank): Through Hole

Electrical Specifications:

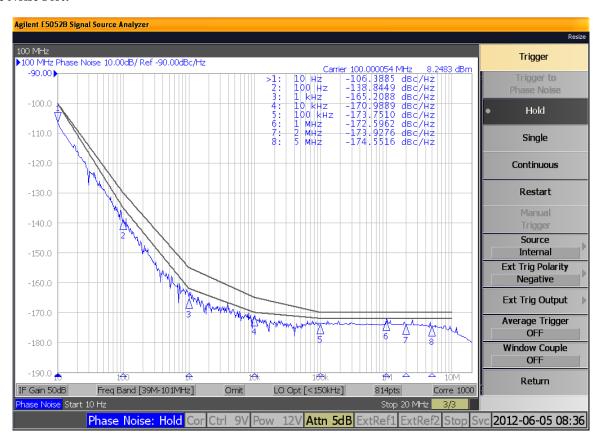
Parameter	Symbol	Min.	Typ.	Max. U		Units	Conditions
Frequency	F		100			MHz	
		F	requency	Stabilities			
Operating Temperature		0		+80		°C	Contact factory for other temperature range options.
Storage Temperature		-55		+8	85	°C	
vs Temperature	$\Delta F_T/F$	-100		100 ppb		ppb	Over the operating temperature range
vs. Supply Voltage	$\Delta F_{VDD}/F$	-30		+30 ppb		ppb	±5% change in voltage
Aging 10 Years		-1		+1 ppm		ppm	After 30-days Power On
Short Term Stability: ADEV			1 x 10 ⁻¹⁰				Tau = 0.1 to 1.0 sec
			RF O	utput			
Output Type		Sinewave					
Output Load			50	Ω		Ω	±10%
Level	V_{OH}		+9			dBm	
Tuning Voltage		0		10 V		V	Contact factory for other tuning range options.
Tuning Range		Sufficient for 10-years aging adjustment					
Modulation Bandwidth		1		kHz			3dB
Vref			+10	•		V_{DC}	≤ 0.5mA
Harmonics				-25 -75		dBc	
Spurious			0.11		75	dBc	
	1	Į.	Other Par		0 1 0	1	T
				Option A	Option B		0.1011.000
SSB Phase Noise				-100	-95		@ 10Hz Offset
(under static conditions)				-132	-127	dBc/Hz	@ 100Hz Offset
				-162 -170	-155 -163		@ 1kHz Offset @ 10kHz Offset
				-170	-165		@ 100kHz Offset
G-sensitivity	+		1.5			ppb/g	@ 100kHz Offset
Warm-up Time			1.5	3 M		Minutes	@ 25°C, relative to 2-hrs after turn-on following 24-hrs off. To within ±0.1ppm of final frequency.
	St	apply Vo	ltage & P	ower Cons	umption		
Supply Voltage	V _{DD}	11.4	12	12.6 V _{DC}		V _{DC}	Contact factory for other supply voltage options.
Power Consumption				1.5		Watts	Steady state @ 25°C in still air
1 ower consumption				3.6		Watts	@ warm-up

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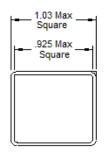
Phase Noise Plot:

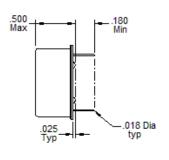


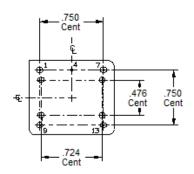
Mechanical Layout & Pin Information:

Pin Assignments Pin 1: RF Output Pin 4: Case Ground Pin 7: VTune

Pin 13: VRef Pin 19: Supply Voltage







Data Sheet Revision Table:

Date	Rev.	Author	Details of Revision
07-30-12	0	MM	Original Draft.

^{**} Please check website for environmental conditions and solder reflow profile.