

1703 E. Highway 50 Yankton, SD 57078 USA

Phone: 800-762-8800 or 605-665-9321 Fax: 605-665-1709

Website: www.mtronpti.com

SPECIFICATION FOR HCMOS COMPATIBLE SMT OSCILLATOR MtronPTI P/N M2002S272

Electrical Specifications:

| Parameter Symb | | Min. | Тур. | Max. | Units | Conditions |
|-------------------------|--------------------------------|---------------------|-----------|---------------------|-------------|---|
| Frequency of Operation | Fo | | 20.000000 | | MHz | |
| Frequency Tolerance | | -25 | | +25 | | @ +23°C ± 3°C |
| Frequency Stability | ΔF/F | -100 | | +100 | ppm | Includes initial calibration tolerance, deviation over operating temperature, shock, vibration, supply, load and aging. |
| Operating Temperature | TA | -55 | | +125 | °C | |
| Storage Temperature | Ts | -55 | | +125 | °C | |
| Supply Voltage | V_{DD} | 2.97 | 3.3 | 3.63 | > | |
| Supply Current (1) | I_{DD1} | | | 20 | mA | Output enabled |
| Supply Current (2) | I_{DD2} | | | 4 | mA | Output disabled |
| Output Type | | HCMOS Compatible | | | | |
| Output Load | | | | 15 | pF | |
| Symmetry (duty cycle) | T_DC | 40 | | 60 | % | Ref to ½ V _{DD} |
| Logic "1" Level | Vон | 90% V _{DD} | | | V | HCMOS load |
| Logic "0" Level | V_{OL} | | | 10% V _{DD} | V | HCMOS load |
| Rise/Fall Time | T _R /T _F | | | 10 | ns | From 10% to 90% V _{DD} |
| Tri-State Enable Logic | ViH | 2.0 | | | V | Pad 1 |
| Tri-State Disable Logic | VIL | | | 0.3 | V | Pad 1 |
| Enable Disable Time | Tsu | | | 150 | ns | Pad 1 |

Environmental & Mechanical Requirements:

| Mechanical Shock | Per MIL-STD-202, Method 213, Condition C (100 g's, 6 ms duration, ½ sinewave) |
|---------------------------|--|
| Vibration | Per MIL-STD-202, Method 201 & 204 (10 g's from 10-2000 Hz) |
| Hermeticity | Per MIL-STD-202, Method 112 (1 x 10 ⁻⁸ atm cc/s of Helium) |
| Solderability | Per EIAJ-STD-002 |
| Max. Soldering Conditions | See solder profile, Figure 1 |
| Package Type | 4- pad 5 X 7 X 1.9 mm leadless ceramic. (M2 type) |
| Part Marking | All parts that have completed the tests and screening requirements shall be marked with a yellow color dot on the top cover of the device. |

Testing/Screening Requirements:

| Special Product Testing | All lots supplied to Hamilton Sunstrand shall have all Electrical Specifications as shown above in this drawing verified at –55°C to +125°C. When tested, lot shall demonstrate a percent defective of no greater that 0.5% (99.5% yield). A demonstrated reject rate of greater that 0.5% will be sufficient cause for the lot to be rejected. |
|-------------------------|---|
| Date Code | All parts from one lot shall be from one date code. Product shall be no older than one year from the receiving date of the purchase order from Hamilton Sunstrand. |



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Dimensions, Marking, and Pin Out Information:

| Pad | Function |
|-----|------------------|
| 1 | Tri-state |
| 2 | Ground |
| 3 | Output |
| 4 | +V _{DD} |

| Part Marking | |
|--------------|-----------|
| Line 1 | M2002S272 |
| Line 2 | 20M000 |
| Line 3 | M yywwvv |

| Legend | |
|--------|--------------|
| уу | Year |
| ww | Work week |
| VV | Factory code |

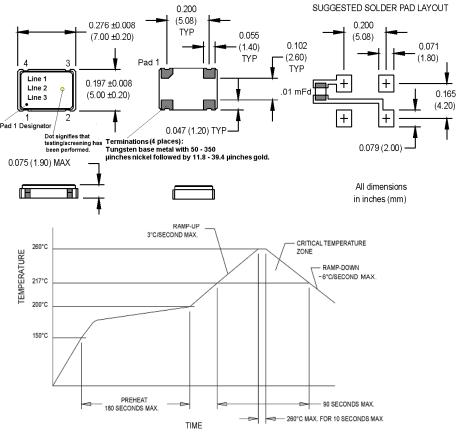


Figure 1

DATA SHEET REVISION TABLE:

| Date | Rev. | Author | Details of Revision |
|----------|------|--------|---|
| 7/23/04 | 0 | RLC | Original release. |
| 1/6/06 | Α | RLC | Added colored dot to part marking to indicate screening performed per Hamilton specs. |
| 2/18/08 | В | RLC | Identified Hamilton Specification Revision "J" on reference information |
| 8/27/09 | С | WNJ | Updated data sheet format. Added Tri-state enable/disable logic levels. |
| 01/16/12 | D | BRM | Updated datasheet to reflect HS drawing Rev M |
| 2/6/14 | Е | MM | Added ITAR statement. |
| 5/20/14 | F | MM | Removed ITAR statement. |
| 05/08/19 | G | MM | Removed customer drawing revision, datasheet will be updated to meet latest revision. |