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SPECIFICATION FOR SMT OSCILLATOR MtronPTI P/N: M2002S704



I. GENERAL & ELECTRICAL REQUIREMENTS:

- FREQUENCY OF OPERATION: 100.000000 MHz
- FREQUENCY STABILITY @ +25°C: ±25 ppm (Includes initial calibration, deviation over operating temperature, load, supply, and aging).
- FREQUENCY VS. AGING: ± 5 ppm/year max. at +25°C.
- OPERATING TEMPERATURE RANGE: -40°C to +85°C
- 5. OPERATING VOLTAGE (Vdd): 3.3 V ± 10%
- OPERATING CURRENT: 50 mA max.
- 7. OUTPUT TYPE: HCMOS/TTL Compatible.
- SYMMETRY: 45/55% ref. to ½ Vdd.
- RISE/FALL TIME: 2.5 nS max. ref. between 20% and 80% Vdd
- 10. OUTPUT LOGIC LEVELS: $V_{OL} = 0.4 \text{ V max}$. $V_{OH} = 90\% \text{ Vdd min}$.
- 11. PERIOD JITTER (1-Sigma): 25 ps RMS max.
- 12. OUTPUT LOAD: 15 pF/5 TTL max.
- 13. START-UP TIME: 10 msec. max.
- 14. STANDBY FUNCTION (Pad 1): Logic "1" or "floating", clock signal output
- Logic "0", output disables to high impedance state

15. STANDBY CURRENT: 15 μA max.

Figure 1

+260°C REFLOW PROFILE (RoHS COMPLIANT SOLDER)

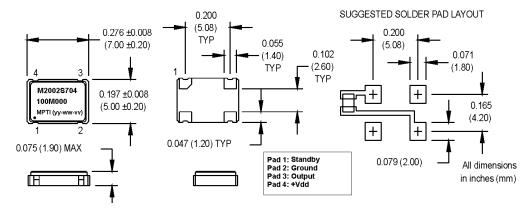
210

180 TIME (Seconds Note: Exceeding these limits can damage the devic

II. ENVIRONMENTAL & MECHANICAL REQUIREMENTS:

- SHOCK: MIL-STD-202, Method 213, Condition C.
- 2. VIBRATION: MIL-STD-202, Methods 201 & 204.
- 3. HERMETICITY: 1 X 10⁻⁸ atm cc/sec min.
- STORAGE TEMPERATURE: -55°C to +125°C
- SOLDERABILITY: Per EIAJ-STD-002 5.
- MAXIMUM SOLDERING CONDITIONS: See Figure 1.
- PACKAGE: 4 pad leadless ceramic package. RoHS compliant.

III. DIMENSIONS:



IV DATA SHEET REVISION TARI E-

IV. DATA CHEET REVISION TABLE.			
Date	Rev.	PCN	Details of Revision
3/30/07	0	N/A	Original release.