

## SPECIFICATION FOR SMT OSCILLATOR MtronPTI P/N M2005S004

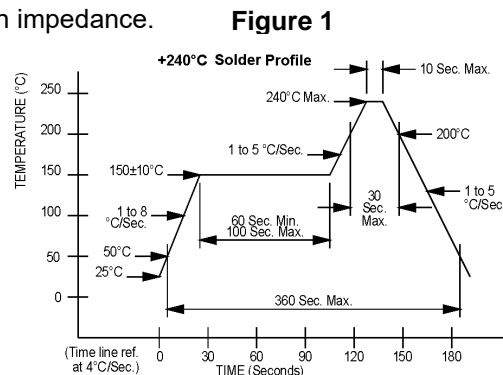
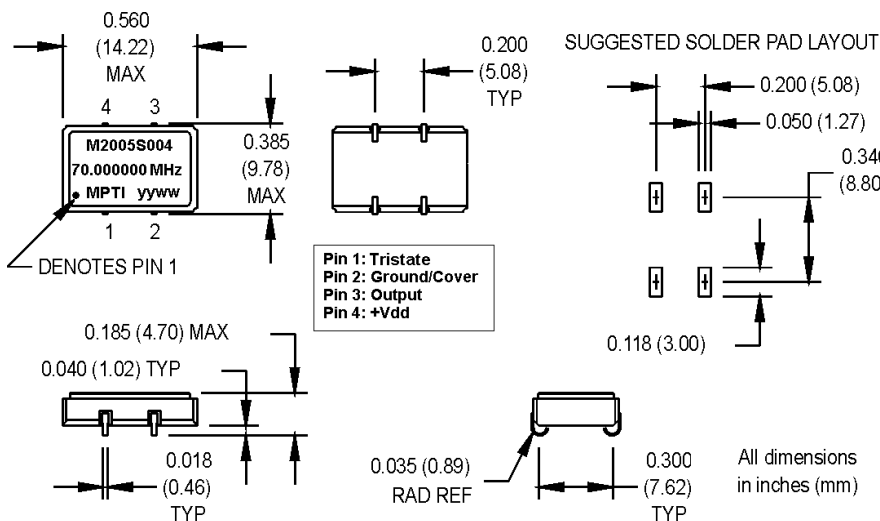
### I. GENERAL & ELECTRICAL REQUIREMENTS:

1. FREQUENCY OF OPERATION 70.000000 MHz
2. FREQUENCY STABILITY:  $\pm 100$  ppm max. (Includes initial tolerance and deviation over temperature)
3. OPERATING TEMPERATURE RANGE: -55°C to +125°C
4. OPERATING VOLTAGE: 3.3 V  $\pm 5\%$
5. OPERATING CURRENT: 35 mA max.
6. OUTPUT TYPE: LVCMOS/LVTTL Compatible
7. SYMMETRY: 40/60% ref. to  $\frac{1}{2}$  Vdd and 1.4 V
8. RISE/FALL TIME: 4 nS max. ref. 10% and 90% and 0.5 V and 2.4 V
9. OUTPUT LOGIC LEVELS:  $V_{OL} = 0.5$  V max.  $V_{OH} = V_{dd} - 0.5$  V min.
10. OUTPUT LOAD: 15 pF HCMOS 10 TTL max.
11. JITTER (Cycle-to-Cycle): 20 pS RMS max. (1 Sigma)
12. TRISTATE FUNCTION (Pin 1): Logic "1" or floating; output enabled.  
Logic "0"; output disabled to a high impedance.

### II. ENVIRONMENTAL & MECHANICAL REQUIREMENTS:

1. SHOCK: MIL-STD-202, Method 213, Condition C.
2. VIBRATION: MIL-STD-202, Methods 201 & 204.
3. HERMETICITY:  $1 \times 10^{-8}$  atm cc/sec min.
4. STORAGE TEMPERATURE: -55°C to +125°C
5. SOLDERABILITY: Per EIAJ-STD-002
6. MAXIMUM SOLDERING CONDITIONS: See Figure 1.
7. PACKAGE: 4- J-lead ceramic. Sn-Pb solder dipped leads.

### III. DIMENSIONS:



Note: Exceeding these limits can damage the device.

### IV. DATA SHEET REVISION TABLE:

Date	Rev.	PCN	Details of Revision
3/10/03	0	N/A	Original release.
7/11/08	A	N/A	Added Honeywell Rev. G information.