



SPECIFICATION FOR HCMOS COMPATIBLE SMT OSCILLATOR MtronPTI P/N M2010S150

Electrical Specifications:

| Parameter | Symbol | Min. | Тур. | Max. | Units | Conditions |
|------------------------------|--------------------------------|---------|---------------|------|-------|-------------------------------------------------------------------------------------|
| Frequency of Operation | Fo | | 10.000000 | | MHz | |
| Frequency Stability | ΔF/F | -70 | | +70 | ppm | Includes initial tolerance, deviation over temperature. |
| Frequency Vs. Supply | | -10 | | +10 | ppm | For ± 0.5 V voltage change. |
| Frequency Vs. Aging | | -10 | | +10 | ppm | First year @ +25°C |
| | | -20 | | +20 | ppm | Over 20 yrs. |
| Total Frequency Deviation | | -100 | | +100 | ppm | Includes initial tolerance, deviation over temperature, supply, and aging. |
| Operating Temperature | TA | -55 | | +125 | °C | |
| Storage Temperature | Ts | -65 | | +125 | °C | |
| Operating Voltage | Vdd | 4.5 | 5.0 | 5.5 | V | |
| Operating Current | IDD | | | 15 | mA | 15 pF load |
| Power Dissipation | | | | 400 | mW | |
| Output Type | | H | CMOS Compatib | ole | | |
| Output Load | | | | 15 | pF | |
| Symmetry (duty cycle) | T _{DC} | 45 | | 55 | % | Ref to 1/2 VDD |
| Logic "1" Level | Vон | 3.0 | | | V | @ 0.6 mA І _{ОН} |
| | Vон | 4.3 | | | V | @0.19 mA І _{ОН} |
| Logic "0" Level | Vol | | | 0.4 | V | @ 0.6 mA Io∟ |
| Rise/Fall Time | T _R /T _F | | | 10 | ns | From 10% to 90% V _{DD} |
| Tri-state Enable Voltage | Vih | 70% Vdd | | | V | Pad 1 |
| Tri-state Disable Voltage | Vil | | | 0.8 | V | Pad 1 |

Environmental & Mechanical Requirements:

| Mechanical Shock | Per MIL-STD-202, Method 213, Condition C (100 g's, 6 ms duration, ½ sinewave) |
|---------------------------|-------------------------------------------------------------------------------|
| Vibration | Per MIL-STD-202, Method 201 & 204 (10 g's from 10-2000 Hz) |
| Thermal Cycle | Per MIL-STD-883, Method 1010, B (-55°C to 125°C, 15 min. dwell, 10 cycles) |
| Hermeticity | Per MIL-STD-202, Method 112 (1 x 10 ⁻⁸ atm cc/s of Helium) |
| Solderability | Per EIAJ-STD-002 |
| Max. Soldering Conditions | See solder profile, Figure 1 |
| Package Type | 5 X 7 X 1.9 mm leadless ceramic. RoHS compliant. |





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Dimensions, Marking, and Pin Out Information:

| Pad | Function | 1 | Pa | rt Marking | | | Legend | 7 |
|------------------------------------------------------------------------------|----------------------------------|-----|--------------|------------------------------------------|--------------------------------------------|------------|------------------|-------------------------------------------------|
| 1 | Tri-state | | Line 1 | M2010S150 | | уу | Year | |
| 2 | Ground | | Line 2 | 10M0000 | | ww | Work Week | |
| 3 | Output | | Line 3 | M yywwvv | | vv | Factory code | |
| 4 | +V _{DD} | | | | | | | |
| 4 3 Line 1 Line 2 Line 3 1 2 Pad 1 Design 0.075 (1.90) MAX | I | | 200 5.08) | 0.055 (1.40) 0.10 TYP (2.60 TYP |) | _ | GESTED SOLDER P/ | AD LAYOUT 0.071 (1.80) 0.165 (4.20) |
| | 260°C 217°C 200°C 150°C | 180 | | | RITICAL TEMPE INE POLISEC 90 SECC | OWN OND | | |

DATA SHEET REVISION TABLE:

| Date | Rev. | Author | Details of Revision | | | |
|---------|------|--------|----------------------------------------------------------------------------------------|--|--|--|
| 3/24/10 | 0 | WNJ | Original release. | | | |
| 5/6/10 | А | WNJ | Changed Symmetry to 45/55% max. Changed Stability to +/- 50 ppm. | | | |
| 5/7/10 | В | WNJ | Changed Stability back to +/- 70 ppm. | | | |
| 5/10/10 | С | WNJ | Changed Tri-state Enable/Disable time to 100 ns max. | | | |
| 7/1/10 | D | WNJ | Changed Max. Current from 55 mA to 15 mA. Updated Frequency Stability and Aging specs. | | | |
| 7/9/10 | E | WNJ | Changed Tri-state disable voltage spec from 30% Vdd max. to 0.8 V max. | | | |
| 8/22/12 | F | MM | Added customer part number | | | |