

SPECIFICATION FOR 2.5 V LVDS SMT OSCILLATOR MtronPTI P/N: M2101S020

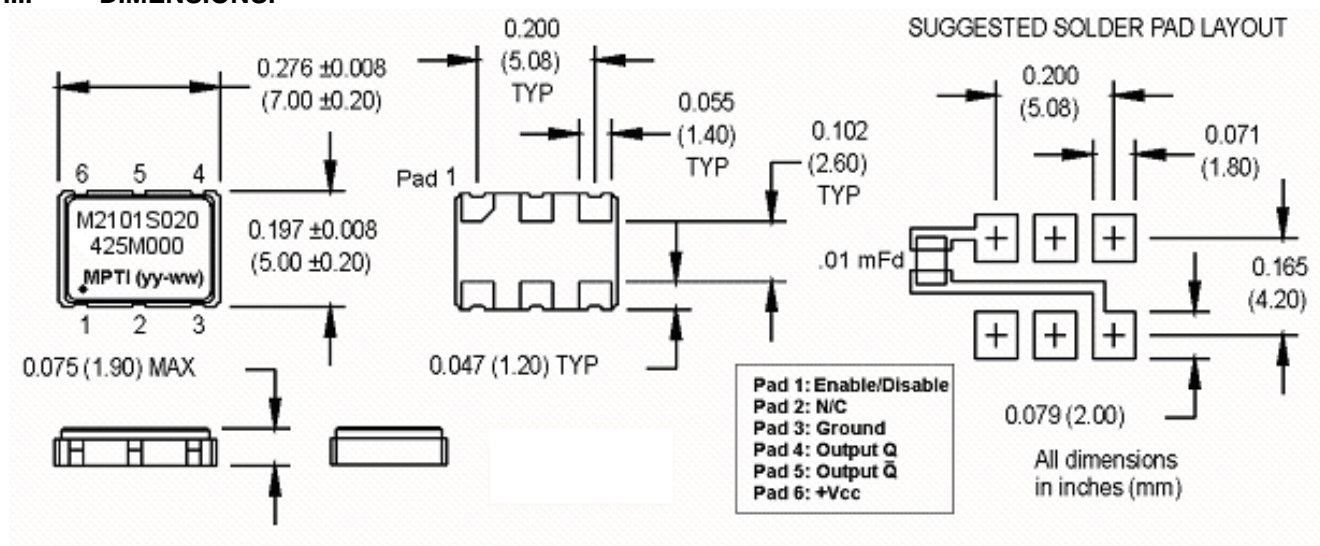
I. GENERAL & ELECTRICAL REQUIREMENTS:

1. TECHNOLOGY: 3rd Overtone Crystal
2. FREQUENCY OF OPERATION: 425.000000 MHz
3. OVERALL FREQUENCY STABILITY: ± 25 ppm
(Includes initial tolerance, deviation over temperature, supply voltage, shock and vibration)
4. AGING: ± 3.0 ppm/1st year, ± 1.0 ppm/every year thereafter
5. OPERATING TEMPERATURE RANGE: -20°C to +85°C
6. OPERATING VOLTAGE (Vcc): 2.5 V $\pm 5\%$
7. OPERATING CURRENT: 125 mA max.
8. OUTPUT TYPE: LVDS
9. SYMMETRY: 45/55% ref. to 50% of waveform
10. RISE/FALL TIME: 0.5 ns max. ref. to 20% to 80%
11. DIFFERENTIAL VOLTAGE: 250 mV min., 450 mV max.
12. COMMON MODE OUTPUT VOLTAGE: 1.2 V typical
13. OUTPUT LOAD: 100 Ω differential load
14. PHASE JITTER: 0.3 ps RMS max. (12 KHz to 20 MHz)
15. ENABLE/DISABLE (Pad 1): Logic "high" or "floating", clock signal output
Logic "low", output disables to high impedance state

II. ENVIRONMENTAL & MECHANICAL REQUIREMENTS:

1. SHOCK: MIL-STD-202, Method 213, Condition C.
2. VIBRATION: MIL-STD-202, Methods 201 & 204.
3. STORAGE TEMPERATURE: -55°C to +125°C
4. SOLDERABILITY: Per EIAJ-STD-002
5. HERMETICITY: 1 X 10⁻⁸ atm cc/sec min.
6. MAXIMUM SOLDERING CONDITIONS: See Section IV
7. PACKAGE: 6- pad leadless ceramic. RoHS 6 compliant.

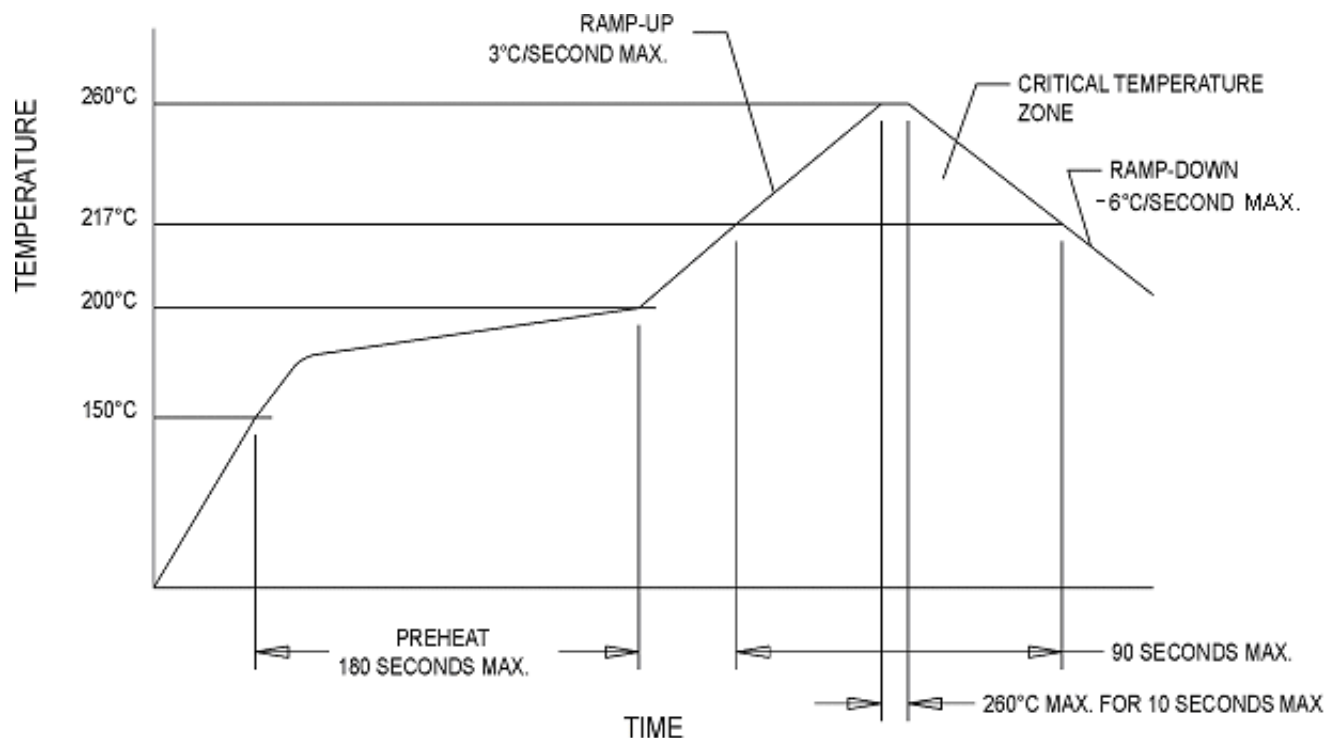
III. DIMENSIONS:



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IV. SOLDERING PROFILE:



V. DATA SHEET REVISION TABLE:

Date	Rev.	Orig.	Details of Revision
12/29/08	0	DDM	Original release.
2/23/09	A	DDM	Added Redback part number and aging numbers.