M2180 Series

5x7 mm, 1.8 Volt, HCMOS/TTL, Clock Oscillator

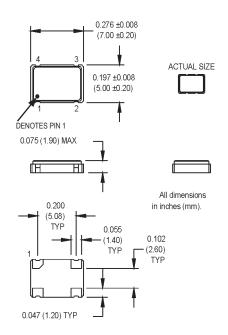




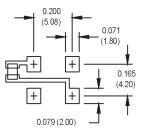




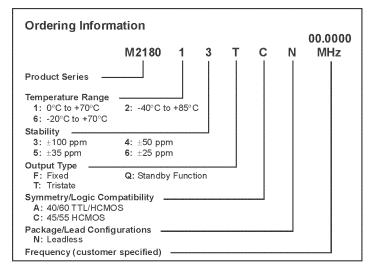
- 1.8 Volt Operation
- Standby Option
- · High density boards, low power circuits, portable test sets



SUGGESTED SOLDER PAD LAYOUT



NOTE: A capacitor of value $0.01 \mu F$ or greater between Vdd and Ground is recommended.



M2180Sxxx - Contact factory for datsheet

Pin Connections

PIN	FUNCTION				
1	N/C, Tri-state or Standby				
2	Ground				
3	Output				
4	+Vdd				

	PARAMETER	Symbol	Min.	Тур.	Max.	Units	Condition
Electrical Specifications	Frequency Range	F	1.0		70	MHz	See Note 1
	Frequency Stability	∆F/F	(See Ordering Information)				
	Operating Temperature	TA	(See Ordering Information)				
	Storage Temperature	Ts	-55		+125	°C	
	Input Voltage	Vdd	1.62	1.8	1.98	V	
	Input Current	ldd			20	mA	
	Standby Current				10	μΑ	Standby Mode
	Symmetry (Duty Cycle)		(See Ordering Information)				½ Vdd
	Load				30/10	pF/TTL	
	Rise/Fall Time	Tr/Tf					
	1.000 to 35.328 MHz				10	ns	Ref. 10% - 90% Vdd
	35.328 to 70.000 MHz				6	ns	Ref. 10% - 90% Vdd
	Logic "1" Level	Voh	90% Vdd			V	HCMOS Load
	Logic "0" Level	Vol			10% Vdd	V	HCMOS Load
	Cycle to Cycle Jitter			8	15	ps RMS	1 Sigma
	Standby/Tristate Function	Input Logic "1" or floating; output active					
		Input Logic "0"; output to high-Z					
Environmental	Mechanical Shock	Per MIL-STD-202, Method 213, Condition C					
	Vibration	Per MIL-STD-202, Method 201 & 204					
	Max Soldering Conditions	See solder profile, Figure 1					
	Hermeticity	Per MIL-STD-202, Method 112 (1 x 10° atm.cc/s of helium)					
區	Solderability	Per EIAJ-STD-002					

^{1.} Not all frequencies are available. Please contact factory for availability.

MtronPTI reserves the right to make changes to the product(s) and service(s) described herein without notice. No liability is assumed as a result of their use or application.

TTL Load - see load circuit diagram #1. HCMOS Load - see load circuit diagram #2.