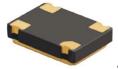
M2250 Series

5x7 mm, 2.5 Volt, HCMOS/TTL, Clock Oscillator

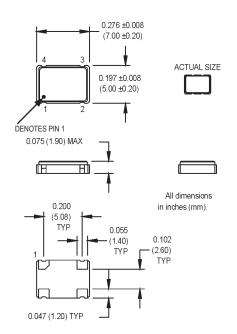




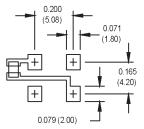




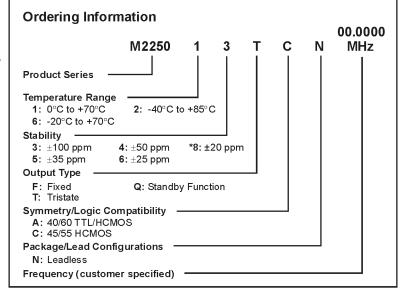
- 2.5 Volt Operation
- Standby Option
- High density boards, low power circuits, portable test sets



SUGGESTED SOLDER PAD LAYOUT



NOTE: A capacitor of value 0.01 μ F or greater between Vdd and Ground is recommended.



* 0°C to +70°C only

M2250Sxxx - Contact factory for datasheet.

Pin Connections

| PIN | FUNCTION |
|-----|---------------------------|
| 1 | N/C, Tri-state or Standby |
| 2 | Ground |
| 3 | Output |
| 4 | +Vdd |

| | PARAMETER | Symbol | Min. | Тур. | Max. | Units | Condition |
|---------------------------|---|--|----------------------------|------|---------|--------|--------------------|
| Electrical Specifications | Frequency Range | F | 1.0 | | 125 | MHz | See Note 1 |
| | Frequency Stability | ∆ F/F | (See Ordering Information) | | | | |
| | Operating Temperature | TA | (See Ordering Information) | | | | |
| | Storage Temperature | Ts | -55 | | +125 | °C | |
| | Input Voltage | Vdd | 2.375 | 2.5 | 2.625 | V | |
| | Input Current | ldd | | | 30 | mA | |
| | Standby Current | | | | 10 | μА | Standby Mode |
| | Symmetry (Duty Cycle) | | (See Ordering Information) | | | | |
| | Load | | | | 15/10 | pF/TTL | |
| | Rise/Fall Time | Tr/Tf | | | 6 | ns | Ref. 0.25 - 2.25 V |
| | Logic "1" Level | Voh | 90% Vdd | | | V | HCMOS Load |
| | Logic "0" Level | Vol | | | 10% Vdd | V | HCMOS Load |
| | Cycle to Cycle Jitter | | | 8 | 15 | ps RMS | 1 Sigma |
| | Standby/Tristate Function | Input Logic "1" or floating; output a Input Logic "0"; output to high-Z | | | active | | |
| Environmental | Mechanical Shock | Per MIL-STD-202, Method 213, Condition C | | | | | |
| | Vibration | Per MIL-STD-202, Method 201 & 204 | | | | | |
| | Hermeticity | Per MIL-STD-202, Method 112 (1 x 10° at m.cc/s of helium) | | | | | |
| | Solderability | y Per EIAJ-STD-002 | | | | | |
| ᇤ | Max Soldering Conditions See solder profile, Figure 1 | | | | | | |

^{1.} Not all frequencies are available. Please contact factory for availability.

TTL Load - see load circuit diagram #1. HCMOS Load - see load circuit diagram #2.

MtronPTI reserves the right to make changes to the product(s) and service(s) described herein without notice. No liability is assumed as a result of their use or application.





