

SPECIFICATION FOR SMT VCXO MtronPTI P/N M3006S075

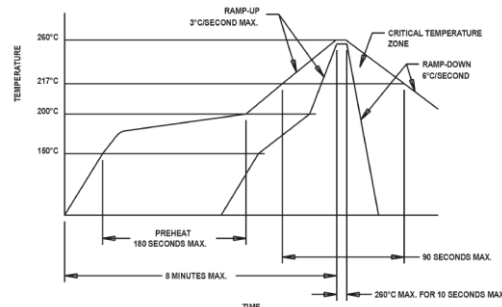
I. GENERAL & ELECTRICAL REQUIREMENTS:

1. FREQUENCY OF OPERATION: 64.000000 MHz
2. FREQUENCY STABILITY: ± 25 ppm max. (Includes initial tolerance @+25°C, deviation over temperature, aging, load & supply variations, shock and vibration effects – VC= 1.65V).
3. OPERATING TEMPERATURE RANGE: -40°C to +85°C
4. OPERATING VOLTAGE: 3.3 V $\pm 5\%$
5. OPERATING CURRENT: 50 mA max.
6. OUTPUT TYPE: HCMOS Compatible
7. SYMMETRY: 40/60% ref. to $\frac{1}{2}$ Vdd
8. RISE/FALL TIME: 5 nS max. ref. 10% to 90% Vdd
9. OUTPUT LOGIC LEVELS: V_{OL}= 10% Vdd max. V_{OH} = 90% Vdd min.
10. OUTPUT LOAD: 15 pF max.
11. PULLABILITY: ± 50 ppm min.
12. LINEARITY: 15% max. with positive monotonic slope.
13. CONTROL VOLTAGE (Pin 1): 1.65V ± 1.5 V
14. MODULATION BANDWIDTH (- 3 dB): 20 kHz min.
15. STARTUP TIME: 10ms typical
16. PHASE NOISE UNDER VIBRATION: -108dBc/Hz @ 1 kHz maximum (3 g random vibration, 10 Hz to 20 kHz)
17. TRISTATE FUNCTION (Pin 2): Logic level "1" or floating enables output.
Logic level "0" disables output to high impedance.

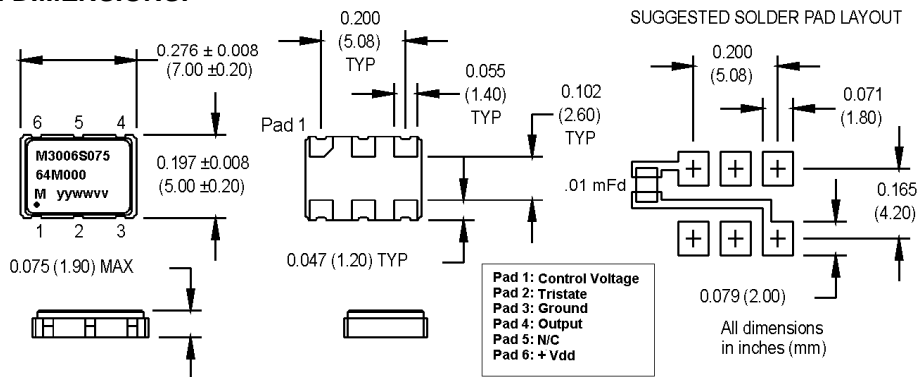
II. ENVIRONMENTAL & MECHANICAL REQUIREMENTS:

1. SHOCK: MIL-STD-202, Method 213, Condition C.
2. VIBRATION: MIL-STD-202, Methods 201 & 204.
3. HERMETICITY: 1 X 10⁻⁸ atm cc/sec min.
4. STORAGE TEMPERATURE: -55°C to +85°C
5. SOLDERABILITY: Per EIAJ-STD-002
6. MAXIMUM SOLDERING CONDITIONS: See Figure 1.
7. PACKAGE: 5 X 7 mm, 6 –pad leadless ceramic package.
RoHS compliant.

Figure 1



III. DIMENSIONS:



IV. DATA SHEET REVISION TABLE:

Date	Rev.	Author	Details of Revision
4/14/05	0	RLC	Original release.
7/15/09	A	WNJ	Added RoHS compliance information & updated Max. Soldering Conditions.