



SPECIFICATION FOR RoHS 6 COMPLIANT SMT VCTCXO MtronPTI P/N M6056S017

Electrical Specifications:

Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions
Frequency of Operation	Fo		16.000000		MHz	
		Fr	equency Stabi	lity		
Initial Tolerance	$\Delta F/F$	-1.5		+1.5	ppm	@ +25°C, with Vc = 1.5V
Fraguanay Stability				105		$(F_{max} - F_{min})/2$, over operating
Frequency Stability	$\Delta \Gamma / \Gamma$			+0.5	ppm	temperature range
Frequency vs. Supply		-0.2		+0.2	ppm	For a ±5% supply voltage change
Frequency vs. Load		-0.2		+0.2	ppm	For a ±10% load change
Aging	FA	-1.0		+1.0	ppm	Per year
			Output			
		C	linned Sine Wa			External DC-cut output capacitor
		0		ve	(0.001 µF) required.	
Output Load			10 kΩ 10 pF			± 10%
Output Levels	V _{OUT}	0.8			V _{pk-pk}	Load =10K // 10 pF
Startup Time	t _{s∪}			10	ms	
Control Voltage	Vc	0.5	1.5	2.5	V	
Tuning Range		±5.0			ppm	
		A	dditional Spec	ifications		
			-85		dBc/Hz	@ 10 Hz Offset
SSB Phase Noise			-110		dBc/Hz	@ 100 Hz Offset
(Under Static Conditions)			-135		dBc/Hz	@ 1 kHz Offset
			-150		dBc/Hz	@ 10 kHz Offset
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Operating Voltage	Vs	2.85	3.00	3.15	VDC	
Operating Current	ls			2.0	mA	

Environmental & Mechanical Requirements:

Operating Temperature	TA	-30		+85	°C	
Storage Temperature	Ts -55 +105 °C					
Shock	Per MIL-STD-202, Method 213, Condition C					
Vibration	Per MIL-STD-202, Methods 201 & 204					
Maximum Soldering Conditions	See Figure 1.					
Solderability	See solder profile, Figure 1					
Package Type	3.2 x 2.5 mm, 4-pad Ceramic Leadless Chip Carrier. RoHS Compliant					





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Dimensions, Pin Out and PAD Layout Information:

Part Marking		
Line 1	16M000	
Line 2	M y m vv	



SUGGESTED SOLDER PAD LAYOUT



Legend y Last digit of year m Month letter code vv Factory code

Pad	Function		
1	Tuning Voltage		
2	Ground		
3	Output		
4	+V _{DD}		





Data Sheet Revision Table:

Date	Rev.	Author	Details of Revision		
05/10/19	0	MM	Original Release.		