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SPECIFICATION FOR CLIPPED SINE WAVE SMT VCTCXO MtronPTI P/N M6065S010

Electrical Specifications:

Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions
Frequency of Operation	Fo		20.473280		MHz	
Frequency Tolerance		-2.0		+2.0	ppm	$V_C = 1.5 \text{ V } @ +25 \text{ °C}.$
Frequency Stability	ΔF/F	-2.5		+2.5	ppm	Over operating temperature
Frequency Vs. Supply		-0.2		+0.2	ppm	For a 10% voltage change
Frequency Vs. Load		-0.2		+0.2	ppm	±10% change in load
Frequency vs. Aging	F_A	-1.0		+1.0	ppm	1 st year @ +25 °C.
Operating Temperature	T _A	-40		+85	°C	
Storage Temperature	Ts	-55		+105	°C	
Operating Voltage	Vs	2.85	3.00	3.15	V	
Operating Current	Is			2.0	mΑ	
Output Type		Cli	pped Sine Wave	Э		External DC-cut output capacitor (0.001 µFd) required.
Output Load		10 kΩ ± 10% in parallel with 10 pF ±10% to ground				
Output Levels	V_{Out}	0.8			V_{pk-pk}	Load =10K 10pF ±10%
Pullability		±8			ppm	Ref. to frequency with Vc = 1.5
Control Voltage		0.50	1.50	2.50	V	Pad 1

Environmental & Mechanical Requirements:

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Shock	Per MIL-STD-202, Method 213, Condition C	
Vibration	Per MIL-STD-202, Methods 201 & 204	
Maximum Soldering Conditions	See Figure 1.	
Solderability	Per EIAJ-STD-002	
Package Type	6- pad 2.0 X 2.5 X 0.9 mm leadless ceramic. RoHS compliant	



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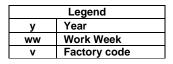


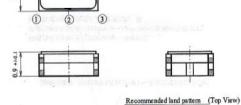
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Dimensions, Pin Out, and Marking Information:

Pin	Function
1	Control Voltage
2	Do Not Connect
3	Ground
4	Output
5	Do Not Connect
6	+V _{DD}

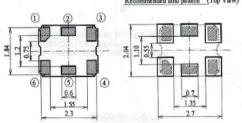
Part Marking		
Line 1	20M	
Line 2	Mywwv	





Line 1

A 0.01 uF capacitor or larger is recommended between $+V_{DD}$ and GND.



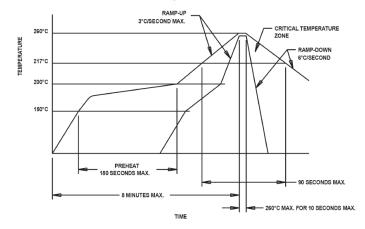


Figure 1

DATA SHEET REVISION TABLE:

Date	Rev.	Author	Details of Revision
09/03/13	0	LEO	Original Release.