

SPECIFICATION FOR SMT TCXO

MtronPTI P/N M6161S030

Electrical Specifications:

Parameter	Symbol	Min.	Typ.	Max.	Units	Conditions
Frequency of Operation	F _o		50.000000		MHz	
Frequency Tolerance		-1.0		+1.0	ppm	@ +25°C
Frequency Stability						
Frequency Stability	$\Delta F/F$	-1.0		+1.0	ppm	(F _{max} – F _{min})/2
Frequency Vs. Reflow		-1.0		+1.0	ppm	
Aging		-2.0		+2.0	ppm	1 st year
Frequency Vs. Supply			± 0.02	± 0.1	ppm	For 5% supply variation
Frequency Vs. Load			± 0.02	± 0.1	ppm	For 5% load change
Output						
Output Type		Clipped Sine Wave				
Output Load		10k Ω // 10pF				
Output Level	V _{OUT}	0.8			V _{pk-pk}	
Tri-state Function		Logic "1", or floating, output Enabled. Logic "0", output Disabled.				Pad 8
Start-up Time	T _{SU}			10	mS	
Additional Specifications						
Phase Noise			-94		dBc/Hz	@ 10 Hz
			-123		dBc/Hz	@ 100 Hz
			-144		dBc/Hz	@ 1 kHz
			-154		dBc/Hz	@ 10 kHz
			-155		dBc/Hz	@ 100 kHz
Supply Voltage & Power Consumption						
Operating Voltage	V _{DD}	3.135	3.3	3.465	V	
Operating Current	I _{DD}			5.0	mA	

Environmental Conditions:

Operating Temperature	T _A	-40		+85	°C	
Storage Temperature	T _S	-55		+125	°C	
Mechanical Shock	Per MIL-STD-202, Method 213, Condition C (100 g's, 6 ms duration, ½ sine wave)					
Vibration	Per MIL-STD-202, Method 201 & 204 (10 g's from 10-2000 Hz)					
Solderability	Per EIAJ-STD-002					
Max. Soldering Conditions	See solder profile, Figure 1					
Package Type	5.0 x 7.0 x 2.0mm, 10-pad Ceramic Leadless Chip Carrier. RoHS Compliant.					

Special Shipping Requirements:

- A single lot date code shall represent each sales order.
- Customer shall purchase lot overruns.

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Mechanical, Marking and Layout Information:

Pad	Function
1	N/C
2	N/C
3	N/C
4	Ground/Case
5	Output
6	N/C
7	N/C
8	Tristate
9	+V _{DD}
10	N/C

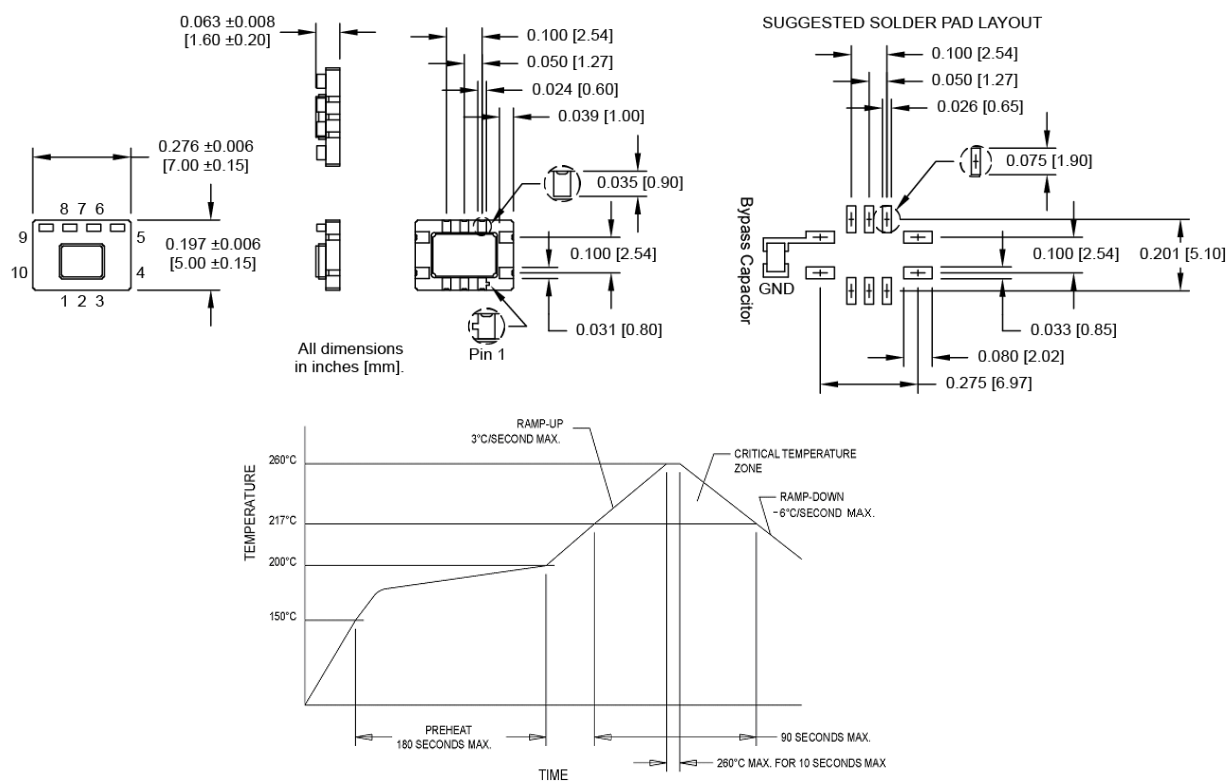


Figure 1

Datasheet Revision Table:

Date	Rev.	Author	Details of Revision
11/18/13	0	MM	Original Release.
1/15/14	A	MM	Added aging specification.