

## SPECIFICATION FOR SMT TCXO

### MtronPTI P/N M6161S033

#### Electrical Specifications:

Parameter	Symbol	Min.	Typ.	Max.	Units	Conditions
Frequency of Operation	F <sub>o</sub>		44.000000		MHz	
Frequency Tolerance		-1.0		+1.0	ppm	@ +25°C
Frequency Stability						
Overall Stability		-15.0		+15.0	ppm	Inclusive of initial calibration, supply voltage, operating temperature, shock, vibration and 30 years of aging @ +55°C.
Frequency Vs. Supply			± 0.02	± 0.1	ppm	For 5% supply variation
Frequency Vs. Load			± 0.02	± 0.1	ppm	For 5% load change
Output						
Output Type		Clipped Sine Wave				
Output Load		10kΩ    10pF			pF	
Output Level	V <sub>OUT</sub>	0.8			V <sub>pk-pk</sub>	
Tri-state Function		Logic “1”, or floating, output Enabled. Logic “0”, output Disabled.				Pad 8
Start-up Time	T <sub>SU</sub>			10	mS	
Additional Specifications						
Phase Noise			-85		dBc/Hz	@ 10 Hz
			-115			@ 100 Hz
			-138			@ 1 kHz
			-152			@ 10 kHz
			-156			@ 100 kHz
Supply Voltage & Power Consumption						
Operating Voltage	V <sub>DD</sub>	3.15	3.3	3.45	V	
Operating Current	I <sub>DD</sub>			5.0	mA	

#### Environmental Conditions:

Operating Temperature	T <sub>A</sub>	-40		+85	°C	Device must operate between -55°C and -40°C without regard to frequency stability specification.
Storage Temperature	T <sub>S</sub>	-55		+125	°C	
Mechanical Shock	Per MIL-STD-202, Method 213, Condition C (100 g's, 6 ms duration, ½ sine wave)					
Vibration	Per MIL-STD-202, Method 201 & 204 (10 g's from 10-2000 Hz)					
Solderability	Per EIAJ-STD-002					
Max. Soldering Conditions	See solder profile, Figure 1					
Package Type	5.0 x 7.0 x 2.0mm, 10-pad Ceramic Leadless Chip Carrier. RoHS Compliant.					

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### Mechanical, Marking and Layout Information:

Pad	Function
1	N/C
2	N/C
3	N/C
4	Ground/Case
5	Output
6	N/C
7	N/C
8	Tristate
9	+V <sub>DD</sub>
10	N/C

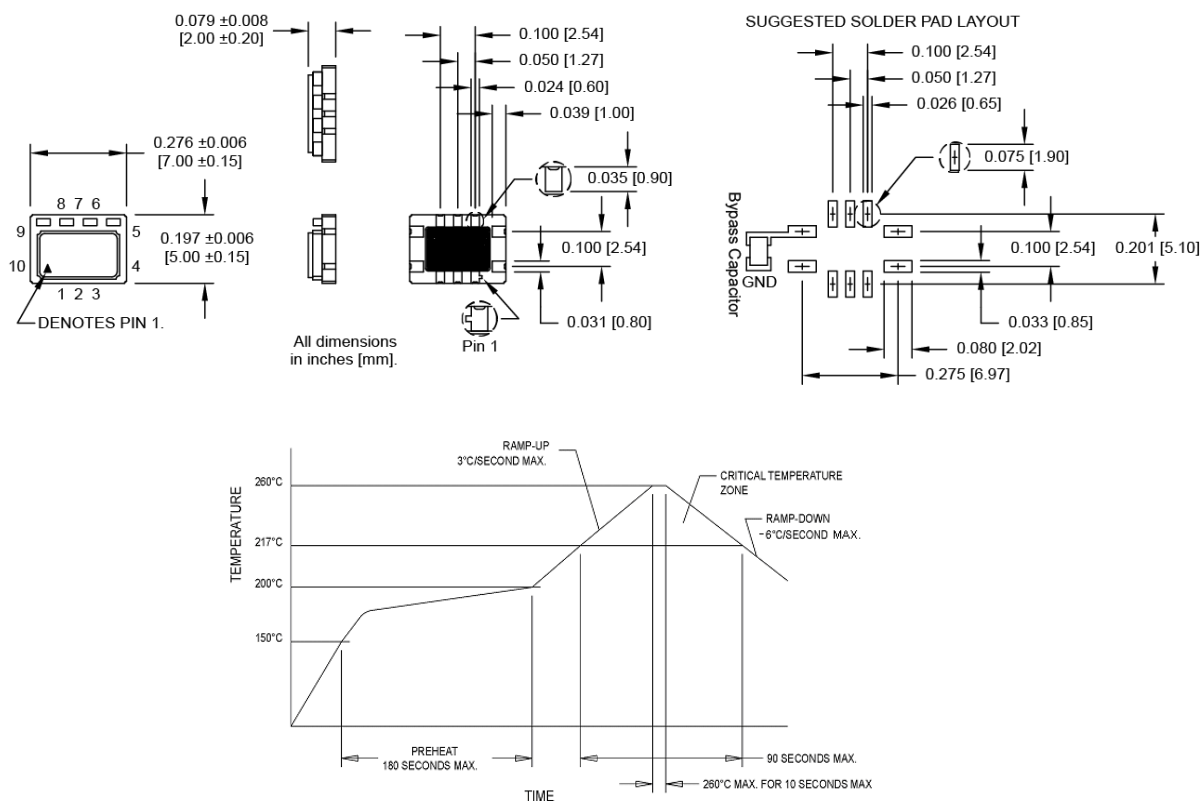


Figure 1

### Datasheet Revision Table:

Date	Rev.	Author	Details of Revision
4/14/14	0	MM	Original Release.