



# PRELIMINARY SPECIFICATION FOR SMT VCTCXO MtronPTI P/N M6161S052

### **Electrical Specifications:**

| Parameter              | Symbol          | Min.   | Тур.         | Max.       | Units | Conditions                               |
|------------------------|-----------------|--|--------------|------------|-------|--|
| Frequency of Operation | Fo              |  | 10.000000    |            | MHz   |  |
| Frequency Tolerance    |                 | -1.0   |              | +1.0       |       | @ +25°C                                  |
|                        |                 | F  | requency Sta | ability    |       |  |
| Frequency Stability    | $\Delta F/F$    |  |              | 0.5        | ppm   | (F <sub>max</sub> - F <sub>min</sub> )/2 |
| Aging                  |                 | -2.0   |              | +2.0       | ppm   | 1st year                                 |
| Frequency Vs. Supply   |                 |  | ± 0.02       | ± 0.1      | ppm   | For 5% supply variation                  |
| Frequency Vs. Load     |                 |  | ± 0.02       | ± 0.1      | ppm   | For 5% load change                       |
|                        | -               |  | Output       |            | -     |  |
| Output Type            |                 | HC   | MOS Compa    | tible      |       |  |
| Output Load            |                 |  |              | 15         | pF    |  |
| Symmetry (duty cycle)  | T <sub>DC</sub> | 40   | 50           | 60         | %     | @ 50% of VDD                             |
| Output Logic Levels    | Vol             |  |              | 20         | % Vcc |  |
| Output Level           | Vон             | 80   |              |            | % Vcc |  |
| Rise/Fall Time         |                 |  |              | 6.5        | ns    | Ref. 10% and 90%                         |
| Tristate Function      |                 | Logic "1", or floating, output Enabled.<br>Logic "0", output Disabled. |              |            |       | Pad 8                                    |
| Start-up Time          | Ts∪             |  |              | 10         | ms    |  |
|                        |                 | Fre  | equency Adju | ustment    |       |  |
| Adjustment Method      |                 | External Voltage   |              |            |       |  |
| Control Voltage Range  | Vc              | +0.3   | 1.65         | +3.0       | VDC   | Pad 10                                   |
| Tuning Range           |                 | ±5   |              |            | ppm   | Positive slope.                          |
| Linearity              |                 |  |              | 5          | %     |  |
| Input Impedance        | ZIN             | 100  |              |            | KΩ    |  |
|                        |                 | Supply Vol   | tage & Powe  | r Consumpt | ion   |  |
| Operating Voltage      | V <sub>DD</sub> | 3.135  | 3.3          | 3.465      | V     |  |
| Operating Current      | I <sub>DD</sub> |  |              | 4.0        | mA    |  |

#### **Environmental Conditions:**

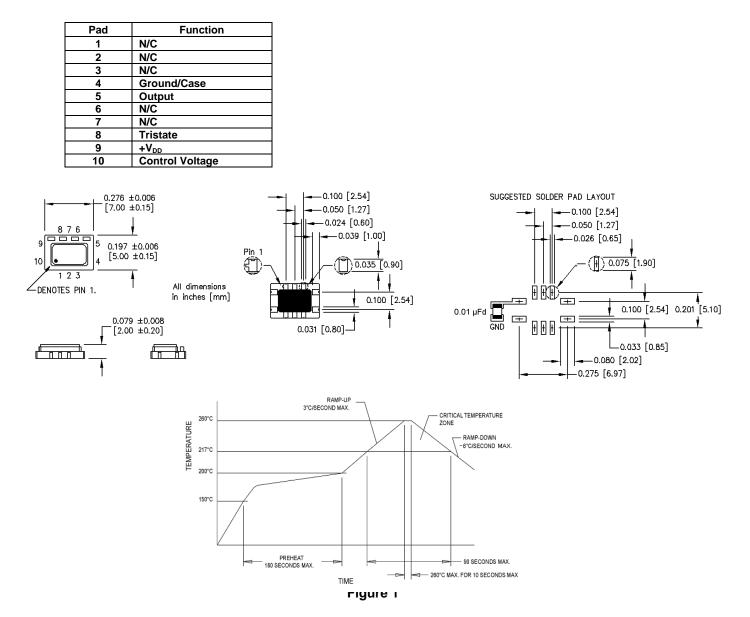
| Operating Temperature     | T <sub>A</sub>   | 0  |  | +70 | °C |  |  |
|---------------------------|--|--|--|-----|----|--|--|
| Storage Temperature       | Ts   | -40  |  | +90 | °C |  |  |
| Mechanical Shock          | Per MIL-ST   | Per MIL-STD-202, Method 213, Condition C (100 g's, 6 ms duration, <sup>1</sup> / <sub>2</sub> sine wave) |  |     |    |  |  |
| Vibration                 | Per MIL-STD-202, Method 201 & 204 (10 g's from 10-2000 Hz)               |  |  |     |    |  |  |
| Solderability             | Per EIAJ-STD-002   |  |  |     |    |  |  |
| Max. Soldering Conditions | See solder profile, Figure 1   |  |  |     |    |  |  |
| Package Type              | 5.0 x 7.0 x 2.0mm, 10-pad Ceramic Leadless Chip Carrier. RoHS Compliant. |  |  |     |    |  |  |





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### Mechanical, Marking and Layout Information:



#### **Datasheet Revision Table:**

| Date     | Rev. | Author | Details of Revision            |
|----------|------|--------|--------------------------------|
| 12/08/16 | 0    | MM     | Original Release.              |
| 12/16/16 | А    | MM     | Corrected Aging specification. |
| 2/15/17  | В    | MM     | Changed to VCTCXO.             |