



SPECIFICATION FOR SMT VCTCXO MtronPTI P/N M6161S056

Electrical Specifications:

Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions	
Frequency	Fo		20.000000		MHz		
Frequency Tolerance		-1.0		+1.0	ppm	@ 25°C, at time of shipment	
Frequency Stabilities							
vs Temperature	$\Delta F/F$	-0.2		+0.2	ppm	Over operating temperature range (Relative to frequency @+25°C)	
vs. Hysteresis		-0.3		+0.3	ppm		
vs. Supply			±0.02	±0.1	ppm	For 5% supply variation	
vs. Load			±0.02	±0.1	ppm	For 5% load variation	
vs. Aging		-0.5		+0.5	ppm	First year.	
	-		Outpu	t	•		
Output Type		HCI	MOS Compati	ble			
Output Load				15	pF		
Symmetry (duty cycle)	TDC	40	50	60	%	@ 50% of VDD	
Output Logic Levels	Vol			20	% V _{DD}		
Output Level	Vон	80			% Vdd		
Rise/Fall Time				6.5	ns	Ref. 10% and 90%	
Tristate Function (Pad 8)		70%Vs			VDC	Pad 8: Output Enabled	
				30%V s		Pad 8: Output Disabled to high-Z	
		F	requency Ad	justment	•		
Adjustment Method External Voltage							
Control Voltage Range	Vc	+0.3	1.65	+3.0	VDC	Pad 10	
Tuning Range		±5			ppm	Positive slope.	
		Ad	ditional Spec	cifications	6		
			-101		dBc/Hz	@ 10 Hz	
			-123		dBc/Hz	@ 100 Hz	
Dhase Noise			-145		dBc/Hz	@ 1 kHz	
Phase Noise			-155		dBc/Hz	@ 10 kHz	
			-155		dBc/Hz	@ 22 kHz	
			-156		dBc/Hz	@ 100 kHz	
G-sensitivity				2.0	ppb/g		
Supply Voltage/Current & Temperature Ranges							
Operating Voltage	Vs	3.135	3.3	3.465	VDC		
Operating Current	ls			4.0	mA		

Environmental Conditions:

Operating Temperature	TA	-40		+85	°C	
Storage Temperature	Ts	-55		+125	°C	
Shock	Per MIL-S	Per MIL-STD-202, Method 213, Condition C (100 g's, 6 <i>msec</i> duration, ½ sinewave)				
Vibration	Per MIL-STD-202, Method 201 & 204 (10 g's from 10-2000Hz)				00Hz)	
Hermeticity	Per MIL-STD-202, Method 112 (1 x 10 ⁻⁸ atm cc/s of Helium) (Crystal Only)				ım) (Crystal Only)	
Solderability	Per EIAJ-STD-002					
Package Type	5.0 x 7.0 x	(1.8mm, 1	0-pad Ceram	nic Leadless	Chip Carrie	r

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Dimensions and Pin-Out Information:

Pad	Function		
1	N/C		
2	N/C		
3	N/C		
4	Ground/Case		
5	Output		
6	N/C		
7	N/C		
8	Tri-state		
9	+V _{DD}		
10	Control Voltage		



Figure 1

DATA SHEET REVISION TABLE:

Date	Rev.	Author	Details of Revision
04/03/18	0	MM	Original release.
12/06/18	А	MM	Updated hysteresis specification limit from ±0.2ppm to ±0.3ppm.

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