

1703 E. Highway 50 Yankton, SD 57078 USA

Phone: 800-762-8800 or 605-665-9321 Fax: 605-665-1709

Website: www.mtronpti.com



SPECIFICATION FOR RoHS 6 COMPLIANT SMT TCXO MtronPTI P/N M6164S020

Electrical Specifications:

Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions
Frequency of Operation	Fo		32.000000		MHz	
Frequency Tolerance		-1.0		+1.0	ppm	@ +25°C
•		Frequency Stabilities				
vs Temperature	ΔF/F			0.5	ppm	(F _{MAX} -F _{MIN})/2 Over operating temperature range
vs Aging		-2.0		+2.0	ppm	1 st year
Frequency vs. Supply	Fvs		± 0.02	± 0.1	ppm	For 5% voltage change
Frequency vs. Load	FL		± 0.02	± 0.1	ppm	For 5% load change
Output Type		HCI	MOS Compati	ible		
Output Load				15	pF	
Symmetry (duty cycle)	T _{DC}	40	50	60	%	@ 50% of VDD
Output Logic Levels	Vol			20	% Vcc	
Output Level	Voн	80			% Vcc	
Rise/Fall Time				6.5	nS	Ref. 10% and 90%
Tri-state Function		Logic "1", or floating, output Enabled. Logic "0", output Disabled.		oled.	Pad 8	
Start-up Time	Tsu	•		10	mS	
		Additional Specifications				
			-90			@ 10Hz Offset
			-118		1	@ 100Hz Offset
SSB Phase Noise			-140		4D a / L I =	@ 1kHz Offset
(Under Static Conditions)			-154		dBc/Hz	@ 10kHz Offset
			-158			@ 100kHz Offset
			-158			@ 1MHz Offset
G-sensitivity			0.6		ppb/g	
•	Supply Voltage & Power Consumption					
Operating Voltage	V_{DD}	3.135	3.3	3.465	V _{DC}	
Operating Current	I _{DD}			5	mA	

Environmental & Mechanical Requirements:

Operating Temperature	T _A	-40		+85	°C	
Storage Temperature	Ts	-55		+125	°C	
Mechanical Shock	Per MIL-STD-202, Method 213, Condition C (100 g's, 6 ms duration, ½ sine wave)					
Vibration	Per MIL-STD-202, Method 201 & 204 (10 g's from 10-2000 Hz)					
Solderability	Per MIL-STD-202, Method 208, except steam aging is not required					
Max. Soldering Conditions	See solder profile, Figure 1					
Package Type	5.0 x 7.0 x 1.6mm, 10-pad Ceramic Leadless Chip Carrier. RoHS Compliant.					



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Mechanical, Marking and Layout Information:

Pad	Function			
1	N/C			
2	N/C			
3	N/C			
4	Ground/Case			
5	Output			
6	N/C			
7	N/C			
8	Tristate			
9	+V _{DD}			
10	N/C			

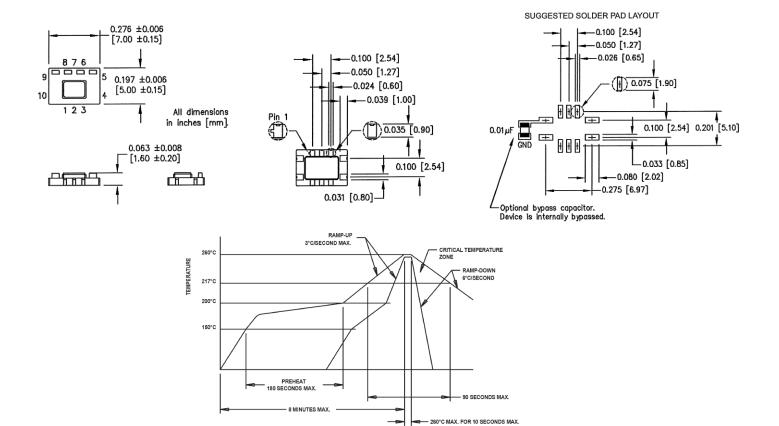


Figure 1

Data Sheet Revision Table:

Date	Rev.	Author	Details of Revision	
10/1/14	0	MM	Original Release.	