

SPECIFICATION FOR RoHS 6 COMPLIANT SMT TCXO

MtronPTI P/N M6164S032

Electrical Specifications:

Parameter	Symbol	Min.	Typ.	Max.	Units	Conditions
Frequency of Operation	F _O		10.000000		MHz	
Frequency Tolerance		-1.0		+1.0	ppm	@ +25°C
Frequency Stabilities						
vs Temperature	ΔF/F			3.0	ppm	(F _{MAX} -F _{MIN})/2 Over operating temperature range
vs Aging		-1.0		+1.0	ppm	1 st year
Frequency vs. Supply	F _{VS}		± 0.02	± 0.1	ppm	For 5% voltage change
Frequency vs. Load	F _L		± 0.02	± 0.1	ppm	For 5% load change
Output						
Output Type		HCMOS Compatible				
Output Load				15	pF	
Symmetry (duty cycle)	T _{DC}	40	50	60	%	@ 50% of VDD
Output Logic Levels	V _{OL}			20	% V _{CC}	
Output Level	V _{OH}	80			% V _{CC}	
Rise/Fall Time				6.5	nS	Ref. 10% and 90%
Tri-state Function		Logic "1", or floating, output Enabled. Logic "0", output Disabled.				Pad 8
Start-up Time	T _{SU}			10	mS	
Additional Specifications						
SSB Phase Noise (Under Static Conditions)			-100		dBc/Hz	@ 10Hz Offset
			-128			@ 100Hz Offset
			-148			@ 1kHz Offset
			-155			@ 10kHz Offset
			-156			@ 100kHz Offset
			-157			@ 1MHz Offset
G-sensitivity			0.6		ppb/g	
Supply Voltage & Power Consumption						
Operating Voltage	V _{DD}	3.135	3.3	3.465	V _{DC}	
Operating Current	I _{DD}			5	mA	

Environmental & Mechanical Requirements:

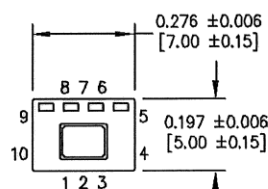
Operating Temperature	T _A	-40		+105	°C	
Storage Temperature	T _S	-55		+125	°C	
Mechanical Shock	Per MIL-STD-202, Method 213, Condition C (100 g's, 6 ms duration, ½ sine wave)					
Vibration	Per MIL-STD-202, Method 201 & 204 (10 g's from 10-2000 Hz)					
Solderability	Per MIL-STD-202, Method 208, except steam aging is not required					
Max. Soldering Conditions	See solder profile, Figure 1					
Package Type	5.0 x 7.0 x 1.6mm, 10-pad Ceramic Leadless Chip Carrier. RoHS Compliant.					

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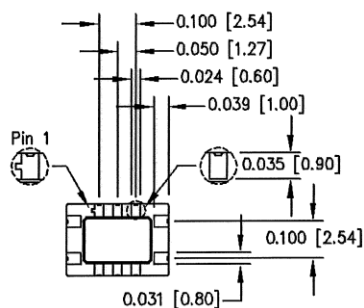
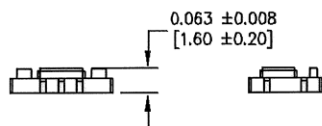
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Mechanical, Marking and Layout Information:

Pad	Function
1	N/C
2	N/C
3	N/C
4	Ground/Case
5	Output
6	N/C
7	N/C
8	Tristate
9	+V _{DD}
10	N/C



All dimensions
in inches [mm].



SUGGESTED SOLDER PAD LAYOUT

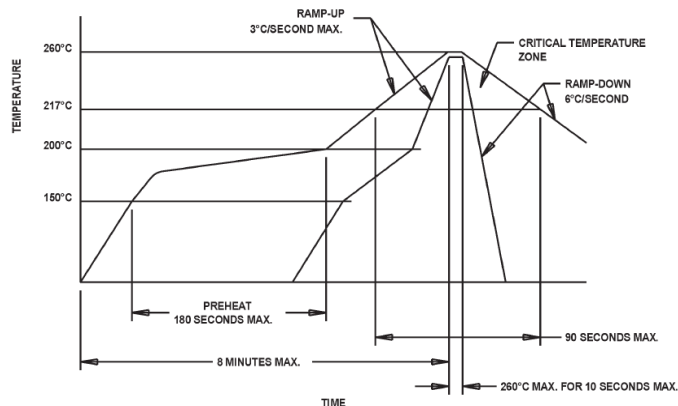
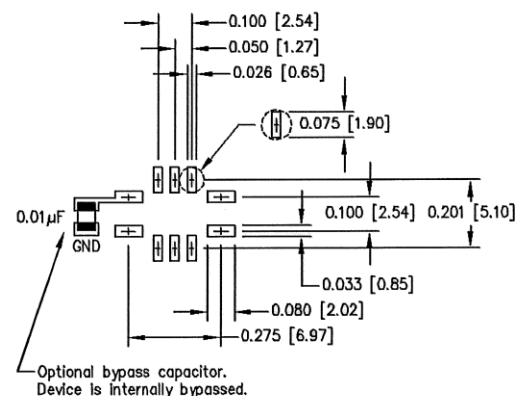


Figure 1

Data Sheet Revision Table:

Date	Rev.	Author	Details of Revision
12-22-15	0	DPD	Original Release.