

1703 E. Highway 50 Yankton, SD 57078 USA

Phone: 800-762-8800 or 605-665-9321 Fax: 605-665-1709

Website: www.mtronpti.com



SPECIFICATION FOR SMT TCXO MtronPTI P/N M6164S039

Electrical Specifications:

| Parameter | Symbol | Min. | Тур. | Max. | Units | Conditions |
|--|-----------------|----------------------|-------------------|------------|-----------------|---|
| Frequency of Operation | Fo | | 10.000000 | | MHz | |
| Frequency Tolerance | | -1.0 | | +1.0 | ppm | @ +25°C |
| | | Fre | equency Stabi | lities | | |
| Frequency vs. Temperature | Δ F/F | | | 1.0 | ppm | (F _{MAX} -F _{MIN})/2 |
| Aging | | -5.0 | | +5.0 | ppm | 10 years |
| Frequency vs. Supply | Fvs | | ± 0.02 | ± 0.1 | ppm | For 5% voltage change |
| Frequency vs. Load | F∟ | | ± 0.02 | ± 0.1 | ppm | For 5% load change |
| | | | Output | | | |
| Output Type | | С | lipped Sine Wa | ave | | |
| Output Load | | 10kΩ // 10 <i>pF</i> | | | | |
| Output Level | Vouт | 0.8 | | | V_{pk-pk} | |
| Tristate Function | | | or floating, outp | Pad 8 | | |
| | | Logic "0", c | output Disabled | 1 44 0 | | |
| Startup Time | Tsu | | | 10 | ms | |
| | | Addi | tional Specific | cations | | |
| SSB Phase Noise (Under Static Conditions) | | | -101 | | | @ 10Hz Offset |
| | | | -123 | | | @ 100Hz Offset |
| | | | -145 | | dBc/Hz | @ 1kHz Offset |
| | | | -155 | | | @ 10kHz Offset |
| | | | -155 | | | @ 100kHz Offset |
| G-sensitivity | | | 1.3 | | ppb/g | |
| | | Supply Volt | age & Power | Consumptio | <u>n</u> | |
| Operating Voltage | V_{DD} | 3.135 | 3.3 | 3.465 | V _{DC} | |
| Operating Current | I _{DD} | | | 4.0 | mA | |

Environmental & Mechanical Requirements:

| Operating Temperature | T _A | -40 | | +85 | °C | |
|---------------------------|--|-----|--|------|----|--|
| Storage Temperature | Ts | -55 | | +125 | °C | |
| Mechanical Shock | Per MIL-STD-202, Method 213, Condition C (100 g's, 6 ms duration, ½ sine wave) | | | | | |
| Vibration | Per MIL-STD-202, Method 201 & 204 (10 g's from 10-2000 Hz) | | | | | |
| Solderability | Per MIL-STD-202, Method 208, except steam aging is not required | | | | | |
| Max. Soldering Conditions | See solder profile, Figure 1 | | | | | |
| Package Type | 5.0 x 7.0 x 2.0mm, 10-pad Ceramic Leadless Chip Carrier. RoHS Compliant. | | | | | |



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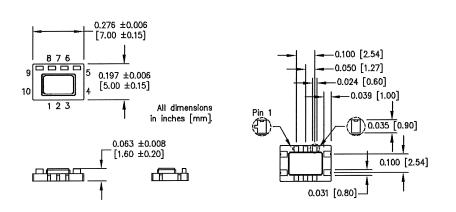
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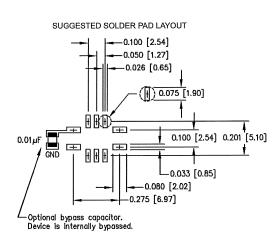


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Mechanical, Marking and Layout Information:

| Pad | Function | | | |
|-----|------------------|--|--|--|
| 1 | N/C | | | |
| 2 | N/C | | | |
| 3 | N/C | | | |
| 4 | Ground/Case | | | |
| 5 | Output | | | |
| 6 | N/C | | | |
| 7 | N/C | | | |
| 8 | Tristate | | | |
| 9 | +V _{DD} | | | |
| 10 | N/C | | | |





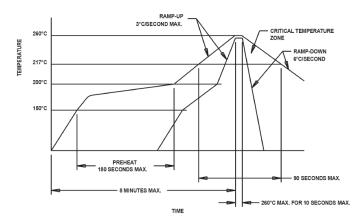


Figure 1

Data Sheet Revision Table:

| Date | Rev. | Author | Details of Revision |
|----------|------|--------|---------------------|
| 05/09/17 | 0 | MM | Original Release. |