

1703 E. Highway 50 Yankton, SD 57078 USA

Phone: 800-762-8800 or 605-665-9321 Fax: 605-665-1709

Website: www.mtronpti.com



# SPECIFICATION FOR SMT TCXO MtronPTI P/N M6171S004

## **Electrical Specifications:**

Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions
Frequency of Operation	Fo		44.000000		MHz	
Frequency Tolerance		-1.0		+1.0	ppm	@ +25°C
		F	requency Sta	bility		
Overall Stability		-15.0		+15.0	ppm	Inclusive of initial calibration, supply voltage, operating temperature, shock, vibration and 30 years of aging @ +55°C.
Frequency Vs. Supply			± 0.02	± 0.1	ppm	For 5% supply variation
Frequency Vs. Load			± 0.02	± 0.1	ppm	For 5% load change
			Output			
Output Type		HC	CMOS Compati	ble		
Output Load			15		pF	
Symmetry (duty cycle)	t <sub>DC</sub>	40	50	60		@ 50% of V <sub>DD</sub>
Logic Level "1"	V <sub>OH</sub>	80			% V <sub>DD</sub>	$I_{OH}/I_{OL} = \pm 4 \text{ mA}, V_{DD} + 3.3 \text{ V}$
Logic Level "0"	$V_{OL}$			20	% V <sub>DD</sub>	$I_{OH}/I_{OL} = \pm 4 \text{ mA}, V_{DD} + 3.3 \text{ V}$
Rise/Fall Time	t <sub>R</sub> /t <sub>F</sub>			6.5	ns	Ref. 10% and 90%
Tri-state Function		Logic "1", or floating, output Enabled. Logic "0", output Disabled.				Pad C
Start-up Time	Tsu			10	mS	
		Add	litional Specif	ications		
			-85			@ 10 Hz
			-115			@ 100 Hz
Phase Noise			-138		dBc/Hz	@ 1 kHz
			-152			@ 10 kHz
			-156			@ 100 kHz
		Supply Vo	Itage & Power			
Operating Voltage	$V_{DD}$	3.15	3.3	3.45	V	
Operating Current	I <sub>DD</sub>			6.5	mA	

### **Environmental Conditions:**

Operating Temperature	TA	-40		+85	°C	Device must operate between - 55°C and -40°C without regard to frequency stability specification.
Storage Temperature	Ts	-55		+125	°C	
Mechanical Shock	Per MIL-STD-202, Method 213, Condition C (100 g's, 6 ms duration, ½ sine wave)					
Vibration	Per MIL-STD-202, Method 201 & 204 (10 g's from 10-2000 Hz)					
Solderability	Per EIAJ-STD-002					
Max. Soldering Conditions	See solder profile, Figure 1					
Package Type	5.0 x 3.2 x 1.58 mm, Ceramic Leadless Chip Carrier. RoHS Compliant (M617x Series)					



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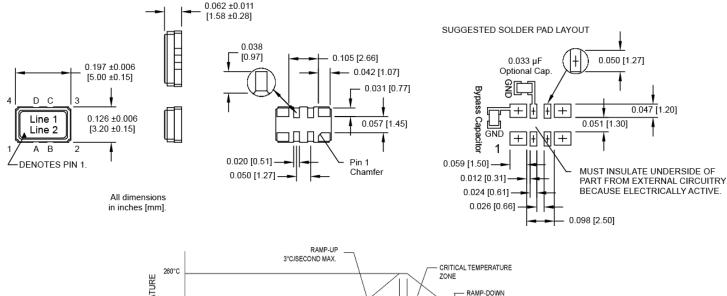
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#### **Mechanical, Marking and Layout Information:**

Pad	Function		
1	N/C		
Α	N/C		
В	N/C		
2	Ground/Case		
3	Output		
С	Tri-state or N/C		
D	Low Phase Noise Capacitor or N/C		
4	Supply Voltage		



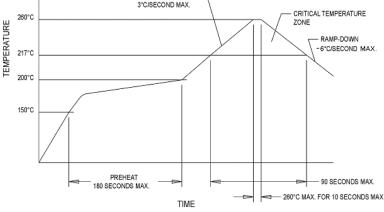


Figure 1

#### **Data Sheet Revision Table:**

Date	Rev.	Author	Details of Revision
05/05/14	0	MM	Original Release.
07/09/14	Α	MM	Changed output type.