

1703 E. Highway 50 Yankton, SD 57078 USA

Phone: 800-762-8800 or 605-665-9321 Fax: 605-665-1709

Website: www.mtronpti.com



# SPECIFICATION FOR RoHS 6 COMPLIANT SMT VCTCXO MtronPTI P/N M6181S001

## **Electrical Specifications:**

Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions
Frequency of Operation	Fo		10.0000000		MHz	
Initial Accuracy	Fı	-1.0		+1.0	ppm	@ 25°C
Frequency Stability	ΔF/F	-2.5		+2.5	ppm	Over operating temperature (F <sub>Max</sub> – F <sub>Min</sub> )/2
Frequency Vs. Reflow		-1.0		+1.0	ppm	
Frequency Vs. Supply				± 0.2	ppm	For 5% supply change
Frequency Vs. Load				± 0.2	ppm	For 5% load change
Operating Temperature	T <sub>A</sub>	-40		+85	Ŝ	
Operating Voltage	$V_{DD}$	3.135	3.3	3.465	V	
Operating Current	$I_{DD}$			3.0	mA	
Output Type		HC	MOS Compatib	le		
Output Load				15	pF	
Symmetry (duty cycle)	T <sub>DC</sub>	45	50	55	%	@ 50% of V <sub>DD</sub>
Logic "1" Level	Vон	80% V <sub>DD</sub>			V	HCMOS load
Logic "0" Level	Vol			20% V <sub>DD</sub>	V	HCMOS load
Rise/Fall Time	$T_R/T_F$			7	nS	From 10% to 90% V <sub>DD</sub>
Frequency Adjust Range		± 9.2			ppm	Over Control Voltage Range. Pad 1.
Control Voltage Range		0.3	1.65	3.0	V	Pad 1
Linearity				5	%	Positive slope. Pad 1
Input Leakage Current		-50		+50	μΑ	Pad 1
Input Resistance		100			kΩ	Pad 1
Modulation Bandwidth		2			kHz	Pad 1
Tri-state Enable Logic		70% V <sub>DD</sub>			V	Pad C
Tri-state Disable Logic				30% V <sub>DD</sub>	V	Pad C. Output to high-Z
Phase Noise			-102		dBc/Hz	@ 10 Hz
			-132		dBc/Hz	@ 100 Hz
			-150		dBc/Hz	@ 1 kHz
			-155		dBc/Hz	@ 10 kHz
			-156		dBc/Hz	@ 100 kHz

#### **Environmental Conditions:**

Shock	Per MIL-STD-202, Method 213, Condition C (100 g's, 6 ms duration, ½ sinewave)
Vibration	Per MIL-STD-202, Method 201 & 204 (10 g's from 10-2000 Hz)
Hermeticity	Per MIL-STD-202, Method 112 (1 x 10 <sup>-8</sup> atm cc/s of Helium) (Crystal Only)
Storage Temperature	-55°C to +125°C
Solderability	Per EIAJ-STD-002
Max. Soldering Conditions	See solder profile, Figure 1
Package Type	5.0 x 3.2 x 1.58 mm, Ceramic Leadless Chip Carrier



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### Mechanical, Marking and Layout Information:

Pad	Function	
1	Control Voltage	
Α	N/C	
В	N/C	
2	Ground/Case	
3	Output	
С	Enable/Disable or N/C	
D	Low Phase Noise Capacitor or N/C	
4	Supply Voltage	

Par	t Marking
Line 1	M61 ym
Line 2	10M000

Legend	
Υ	Year
m	Month

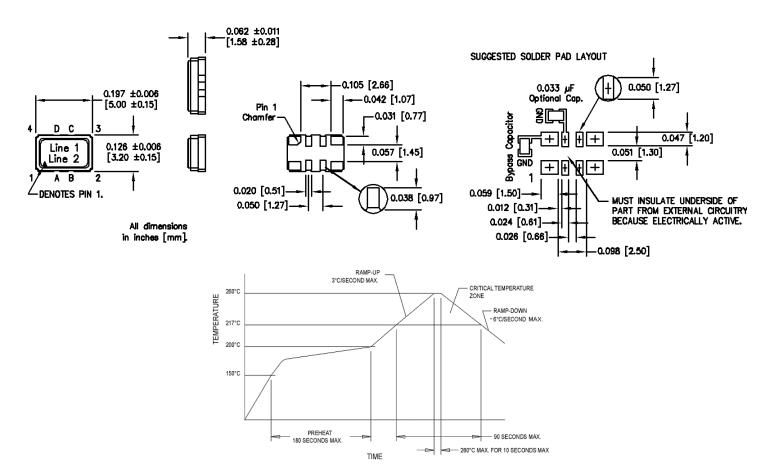


Figure 1

#### **Datasheet Revision Table:**

	Date	Rev.	Author	Details of Revision
	Date	1.01	7101101	Botano et itoriolon
	4/5/13	0	MM	Original release.