





SPECIFICATION FOR 3.3V CMOS SMT TCXO MtronPTI P/N M6300S098

Electrical Specifications:

Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions	
Frequency of Operation	Fo		50.000000		MHz		
Frequency Tolerance		-1.0		+1.0	ppm	@ +25°C	
Frequency Stability							
vs. Temperature	ΔF/F			1.0	ppm	(Max-Min)/2	
va Agina		-3		+3	ppm	1 st year	
vs. Aging		-1		+1	ppm	Per year thereafter.	
			RF Outpu	ıt			
Output Type		НС	CMOS Compatil	ble			
Output Load				15	pF		
Symmetry (duty cycle)	T _{DC}	45		55	%	Ref. to ½ V _{DD}	
Logic "1" Level	Vон	80% V_{DD}			V	HCMOS load	
Logic "0" Level	Vol			20% V _{DD}	V	HCMOS load	
Rise/Fall Time	T _R /T _F			6	ns	From 20% to 80% V _{DD}	
Supply Voltage & Power Consumption							
Operating Voltage	V _{cc}	3.135	3.300	3.465	V		
Operating Current Icc				90	mA		

Environmental Conditions:

Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions	
Operating Temperature	T _A	-40		+85	°C		
Storage Temperature	Ts	-55		+125	°C		
Shock	Per MIL-S	TD-202, M	ethod 213, Con	dition C (10	0 g's, 6 ms	duration, ½ sinewave)	
Vibration	Per MIL-S	Per MIL-STD-202, Method 201 & 204 (10 g's froi		m 10-2000	Hz)		
Thermal Shock	Per MIL-S	Per MIL-STD-883, Method 1011, Condition A					
Thermal Cycle	Per MIL-STD-883, Method 1010, Condition B						
Hermeticity	Per MIL-STD-202, Method 112 (1 x 10 ⁻⁸ atm cc/s of helium)						
Moisture Sensitivity Level (MSL)	MSL 1						
Solderability	Per EIAJ-STD-002						
Max. Soldering Conditions	See Figure 1.						
Package Type 6-pad 5.0 X 7.0 X 1.9 mm leadless ceramic. RoHS compliant.			nt.				

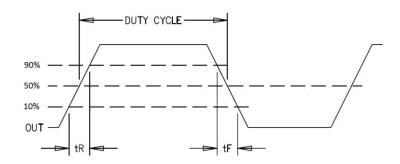




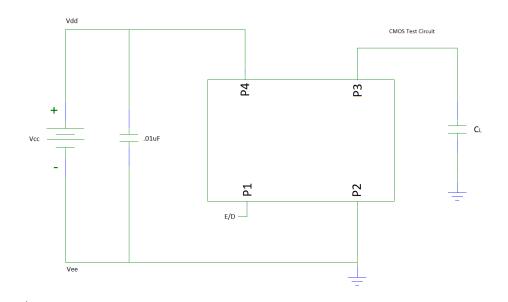


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Output Waveform:



Typical Test Circuit & Load Circuit Diagrams:









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Soldering Conditions:

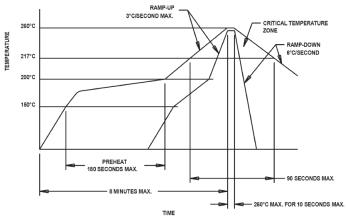
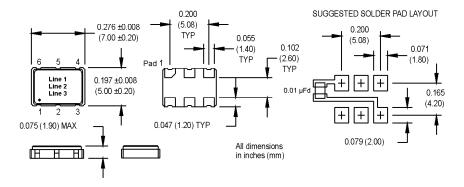


Figure 1

Mechanical, Marking, and Pin Out Information:

Legend		
уу	Year	
ww	Work week	

Pin	Function		
1	N/C		
2	N/C		
3	Ground		
4	Output		
5	N/C		
6	+V _{cc}		



Datasheet Revision Table:

Date	Rev.	Author	Details of Revision	
04/25/18	0	MM	Original release.	