





## **SPECIFICATION FOR 3.3V LVDS SMT TCXO** MtronPTI P/N M6300S124

#### **Electrical Specifications:**

Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions
Frequency of Operation	Fo		25.000000		MHz	
Frequency Tolerance		-1.0		+1.0	ppm	@ +25°C
		F	requency Sta	ability		
vs. Temperature	$\Delta$ F/F			4.6	ppm	(Max-Min)/2
vs. Aging		-3		+3	ppm	1 <sup>st</sup> year
		-1		+1	ppm	Per year thereafter.
			RF Outpu	ıt		
Output Type	Differential LVDS Compatible			npatible		
Output Load		100 Ω Differential			V	
Common Mode Output Voltage			1.2		V	
Differential Output Voltage		250	425	500	mV	LVDS Load
Symmetry (duty cycle)	TDC	45		55	%	Referenced to 1.2 V
Rise/Fall Time	T <sub>R</sub> /T <sub>F</sub>			0.35	nS	From 20% to 80% Vcc
	S	Supply Vo	Itage & Powe	r Consum	nption	
Operating Voltage	V <sub>cc</sub>	3.135	3.3	3.465	V	
Operating Current	Icc			100	mA	

### **Environmental Conditions:**

Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions	
Operating Temperature	TA	-55		+125	°C		
Storage Temperature	Ts	-55		+125	°C		
Shock	Per MIL-S	TD-202, M	ethod 213, Con	dition C (10	0 g's, 6 ms (	duration, ½ sinewave)	
Vibration	Per MIL-S	TD-202, M	ethod 201 & 204	4 (10 g's fro	om 10-2000	Hz)	
Thermal Shock	Per MIL-S	TD-883, M	ethod 1011, Co	ndition A			
Thermal Cycle	Per MIL-S	TD-883, M	ethod 1010, Co	ndition B			
Hermeticity	Per MIL-S	TD-202, M	ethod 112 (1 x '	10 <sup>-8</sup> atm cc/	's of helium)		
Moisture Sensitivity Level	MSL 1						
(MSL)							
Solderability	Per EIAJ-S	STD-002					
Max. Soldering Conditions	See Figure	e 1.					
Package Type	6-pad 5.0 X 7.0 X 1.9 mm leadless ceramic. RoHS compliant.						

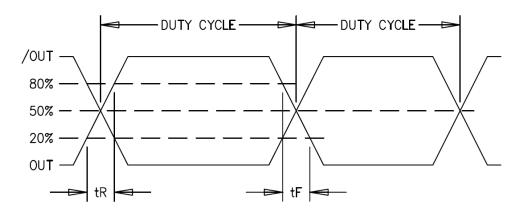




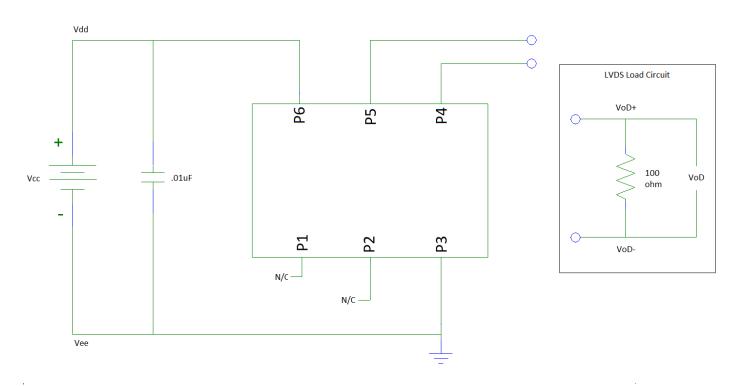


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## **Output Waveform:**



# **Typical Test Circuit & Load Circuit Diagrams:**



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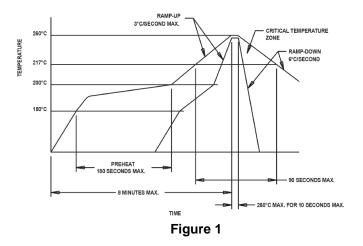




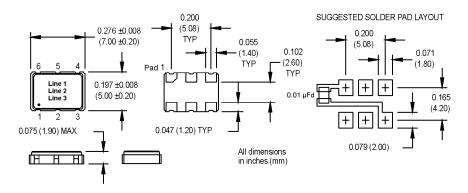


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## **Soldering Conditions:**



### Mechanical, Marking, and Pin Out Information:



#### **Datasheet Revision Table:**

Date	Rev.	Author	Details of Revision		
11/28/18	0	MM	Original release.		