



#### **Electrical Specifications:**

Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions
Frequency of Operation Fo			25.000000		MHz	
Initial Tolerance		-1 +1		+1	ppm	@ +25°C
			Frequency Sta	bility		
vs. Temperature	ΔF/F			4.6	ppm	(Max-Min)/2
			RF Outpเ	ıt		
Output Type		Н	CMOS Compati	ble		
Output Load				15	pF	
Symmetry (duty cycle)	T <sub>DC</sub>	45		55	%	Ref. to ½ V <sub>DD</sub>
Logic "1" Level	V <sub>OH</sub>	80% V <sub>DD</sub>			V	HCMOS load
Logic "0" Level	V <sub>OL</sub>			20%V <sub>DD</sub>	V	HCMOS load
Rise/Fall Time	$T_R/T_F$			6	ns	From 20% to 80% V <sub>DD</sub>
		Ad	ditional Specif	ications		
Tristate Enable Logic		80%V <sub>DD</sub> or N/C			V	Pad 1. Clock Signal Output
Tristate Disable Logic				0.35	V	Pad 1. Output to High-Z
Start-up Time				10	ms	
Supply Voltage & Power Consumption						
Operating Voltage	Vcc	3.135	3.300	3.465	V	
Operating Current	I <sub>cc</sub>			90	mΑ	

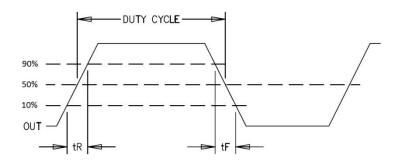
#### **Environmental Conditions:**

Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions
Operating Temperature	T <sub>A</sub>	-55		+125	°C	
Storage Temperature	Ts	-55		+125	°C	
Shock	Per MIL-S	TD-202, M	ethod 213, Con	dition C (10	00 g's, 6 ms	duration, ½ sinewave)
Vibration	Per MIL-S	TD-202, M	ethod 201 & 20	4 (10 g's fro	om 10-2000	Hz)
Thermal Shock	Per MIL-STD-883, Method 1011, Condition A					
Thermal Cycle	Per MIL-STD-883, Method 1010, Condition B					
Hermeticity	Per MIL-S	TD-202, M	ethod 112 (1 x	10 <sup>-8</sup> atm cc/	s of helium)	
Solderability	Per EIAJ-STD-002					
Max. Soldering Conditions	ions See Figure 1.					
Package Type 6-pad leadless ceramic package with (4) Gullwing Leads attached.			ached.			

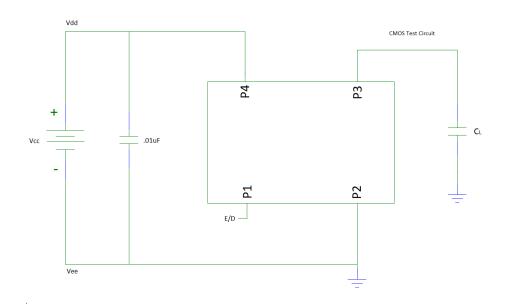




### **Output Waveform:**



### **Typical Test Circuit & Load Circuit Diagrams:**







### **Soldering Conditions:**

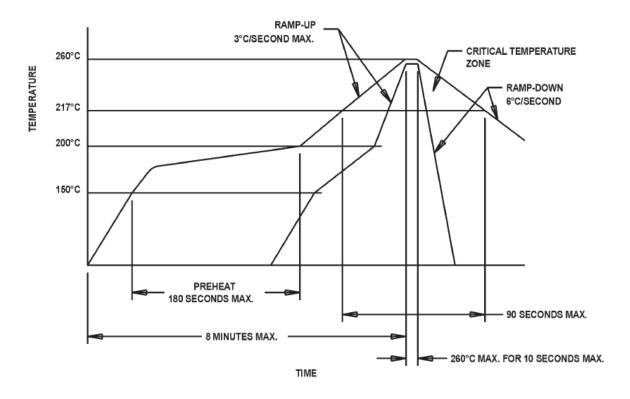


Figure 1



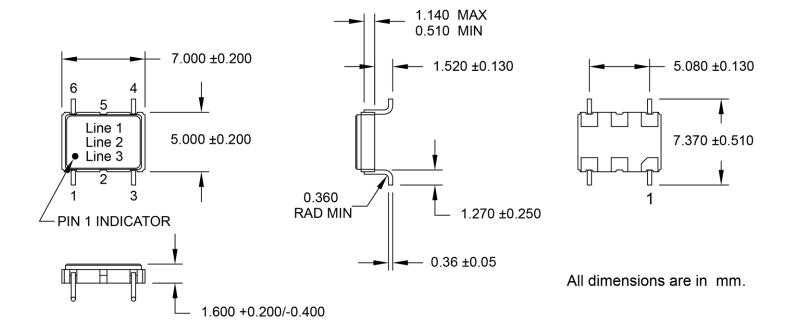


#### Mechanical, Marking, and Pin Out Information:

Part Marking			
Line 1	M6300S128		
Line 2	25M000		
Line 3	yyww		

Legend				
уу	Year			
ww	Work week			

Pin	Function
1	Tristate Control
2	N/C
3	Ground
4	Output
5	N/C
6	+V <sub>CC</sub>
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#### **Datasheet Revision Table:**

Datasticet Nevision Table.					
Date	Rev.	Author	Details of Revision		
08-21-19	Α	BRR	Original release.		
11-25-19	В	BRR	Outline Drawing updated with Gull Wing Leads; Tristate disable logic revised; Part Marking updated		
05-18-20	С	MM	Updated lead configuration.		
08/03/21	D	MM	Added customer part number		
08/11/21	Е	MM	Updated mechanical dimensions.		
08/24/21	F	MM	Updated mechanical dimensions.		