



SPECIFICATION FOR 1.8V LVDS SMT VCTCXO

MtronPTI P/N M6302S008

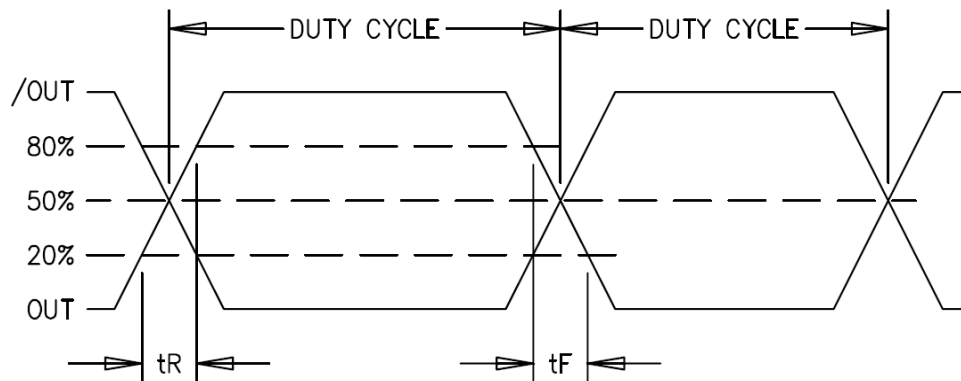
Electrical Specifications:

Parameter	Symbol	Min.	Typ.	Max.	Units	Conditions
Frequency of Operation	F_O		125.000000		MHz	
Frequency Tolerance		-1.0		+1.0	ppm	@ +25°C
Frequency Stability						
vs. Temperature	$\Delta F/F$			1.0	ppm	(Max-Min)/2
vs. Aging		-3		+3	ppm	1 st year
		-1		+1	ppm	Per year thereafter.
RF Output						
Output Type		Differential LVDS Compatible				
Output Load		100 Ω Differential			V	
Common Mode Output Voltage			1.2		V	
Differential Output Voltage		250	350	450	mV	LVDS Load
Symmetry (duty cycle)	T_{DC}	45		55	%	Referenced to 1.25 V
Rise/Fall Time	T_R/T_F			0.35	nS	From 20% to 80% V_{CC}
Enable/Disable Function		80% V_{CC} or N/C			V	Pad 2: Clock signal outputs
				0.5	V	Pad 2: Output disable to High-Z
Frequency Adjustment						
Control Voltage Range		0.18	0.90	1.62	V	Pad 1
Absolute Pull Range (APR)		± 10			ppm	Referenced to nominal frequency including deviation over temperature
Linearity				5	%	Positive Monotonic
Modulation Bandwidth		10			kHz	- 3 dB
Other Parameters						
Phase Noise			-68		dBc/Hz	@ 100 Hz
			-88			@ 1 kHz
			-108			@ 10 kHz
			-133			@ 100 kHz
			-146			@ 1 MHz
			-154			@ 10 MHz
			-156			@ 20 MHz
G-sensitivity			2.5		ppb/g	
Supply Voltage & Power Consumption						
Operating Voltage	V_{CC}	1.71	1.80	1.89	V	
Operating Current	I_{CC}			100	mA	



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Output Waveform:



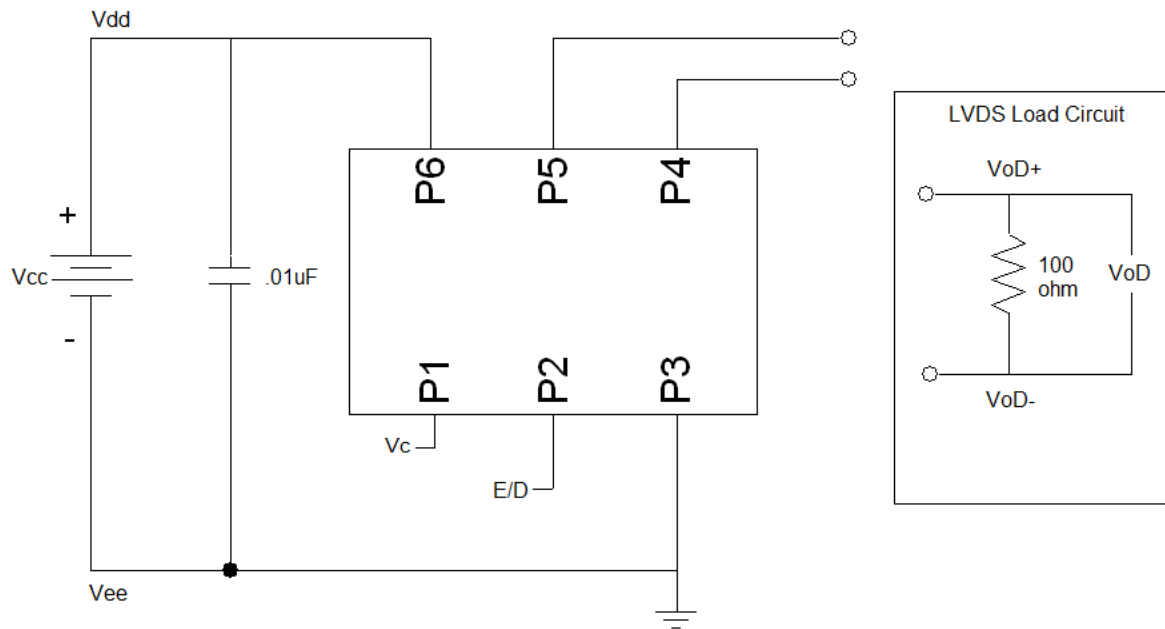
Environmental Conditions:

Parameter	Symbol	Min.	Typ.	Max.	Units	Conditions
Operating Temperature	T _A	-40		+85	°C	
Storage Temperature	T _S	-55		+125	°C	
Shock	Per MIL-STD-202, Method 213, Condition C (100 g's, 6 ms duration, ½ sinewave)					
Vibration	Per MIL-STD-202, Method 201 & 204 (10 g's from 10-2000 Hz)					
Thermal Shock	Per MIL-STD-883, Method 1011, Condition A					
Thermal Cycle	Per MIL-STD-883, Method 1010, Condition B					
Hermeticity	Per MIL-STD-202, Method 112 (1 x 10 ⁻⁸ atm cc/s of helium)					
Moisture Sensitivity Level (MSL)	MSL 1					
Solderability	Per EIAJ-STD-002					
Max. Soldering Conditions	See Figure 1.					
Package Type	6-pad 5.0 X 7.0 X 1.9 mm leadless ceramic. RoHS compliant.					



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Typical Test Circuit & Load Circuit Diagrams:



Soldering Conditions:

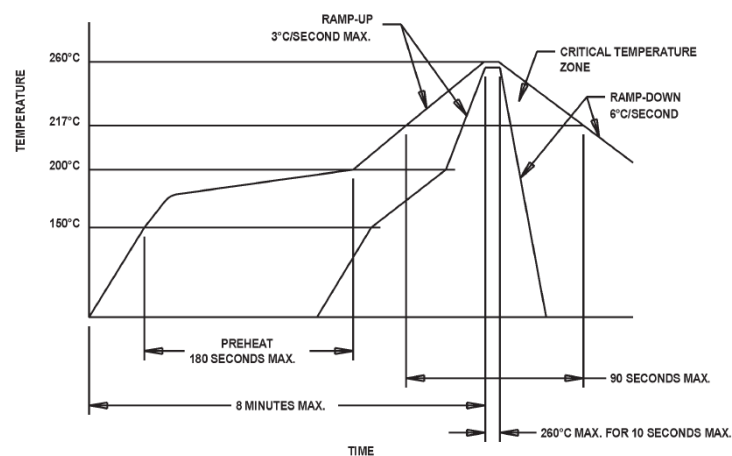


Figure 1



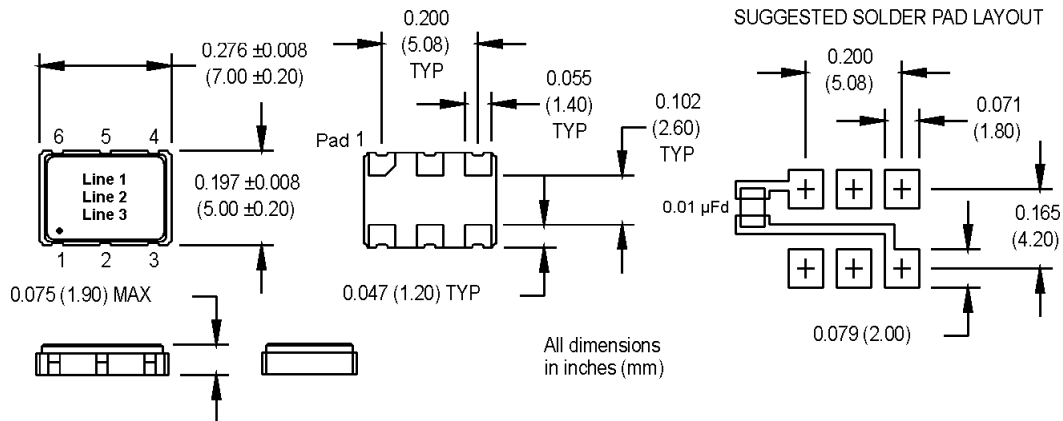
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Mechanical, Marking, and Pin Out Information:

Part Marking	
Line 1	M6302S008
Line 2	125M000
Line 3	MPTI yyww

Legend	
yy	Year
ww	Work week

Pin	Function
1	Voltage Control
2	Enable/Disable
3	Ground
4	Output
5	Complementary Output
6	+V _{CC}



Datasheet Revision Table:

Date	Rev.	Author	Details of Revision
07/10/17	A	DPD	Customer part# added.
06/26/17	0	MM	Original release.