

2525 Shader Road Orlando Florida 32804 USA Phone: 407-298-2000 Fax: 407-293-2979 Website: www.mtronpti.com

MtronPTI MODEL

ENG APPROVAL:

Specification for an LVCMOS Output Thru-Hole OCXO MtronPTI P/N: XO5124-028R

Newpostcom P/N: T. B. D.

MFG APPROVAL

Electrical Specifications

Electrical Specifications:	4					DAIE. 1/28/13
Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions
Nominal Frequency	Fo		10.0000		MHz	
Initial Frequency Accuracy		1		±100	ppb	At the time of shipment, after 30-minutes power on and with V _{TUNE} = +2.5V _{DC}
	d.	Free	quency Stabil	lity		TONE
Over Temperature Range	* 1	2		6.0	ppb	Peak-Peak Over -10°C to +70°C
Aging				±0.5	ppb	Daily (Verified before Shipment)
(after 30-days continuous				±50	ppb	Yearly
operation)				±300	ppb	Over 10-Years
				±400	ppb	Over 15-Years
vs Supply Variation				±1.0	ppb	±5% change in V _{CC}
vs Load Variation				±1.0	ppb	±5% change in load
Retrace (Ref. to Freq. @ Power-off)				±10	ppb	@ 25°C, after 24-hours on, then ≤ 24-hours off and then ≤ 15-minutes from power-on
Short Term Frequency			1	1x10 ⁻¹¹		Tau=1-second
Stability (after stabilization time of 1-hour)	: :			5x10 ⁻¹¹		Tau=100-seconds
			RF Output			
Output Type			LVCMOS	1.7		
Logic Level '1'	V _{OH}	≥ +2.4		≤ +3.3	V_{DC}	
Logic Level '0'	V _{OL}		≤ +0.2		V_{DC}	
Rise/Fall Times	T_R/T_F			≤ 5	ηsec	0.2
Duty Cycle		45		55	%	
Output Load			15	+	рF	
	·		iency Adjustr			
Adjustment Method			External Volta			
Tuning Voltage	V _{TUNE}	0		+5.0	V_{DC}	
Tuning Range		±0.4			ppm	At the time of shipment, sufficient for ≥ 15-years drift, all causes
Tuning Clane			Positive		1:	
Tuning Slope			Positive			
Input Impedance		>100	Positive		kΩ	
				<10	kΩ %	
Input Impedance			B Phase Nois	se		
Input Impedance						@ 10Hz Offset
Input Impedance				se		@ 10Hz Offset @ 100Hz Offset
Input Impedance Linearity				e -120		
Input Impedance Linearity SSB Phase Noise,				-120 -135	%	@ 100Hz Offset



2525 Shader Road Orlando Florida 32804 USA Phone: 407-298-2000 Fax: 407-293-2979 Website: www.mtronpti.com AMEX: LGL



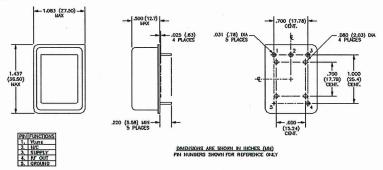
Specification for an LVCMOS Output Thru-Hole OCXO MtronPTI P/N: XO5124-028R Newpostcom P/N: T. B. D.

Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions
	Sı	ipply Voltage	e & Power Co	onsumption		·
Supply Voltage	V _{cc}	4.75	+5.0	5.25	V _{DC}	
Warm-up Current				600	mA	
Steady State Current			220		mA	@ +25°C
		Additio	nal Specifica	tions		
Warm-up Time		a e		8	Minutes	From power-on @ +25°C to within ±10ppb of the frequency @ 1-hour

Physical & Environmental:

Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions	
Operating Temperature	OTR	-10		+70	°C		
Storage Temperature	STR	-50		+90	°C	l l	
Package	Reference Drawing						
ESD	Human Body Model per JESD22-A114E, Meets or Exceeds 2000 Volts, Class 1C						
	Machine Model per JESD22-A115A, Meets or Exceeds 200 Volts, Class M2						
Moisture Sensitivity Level	MSL = 1						
Shock (survival)	Per IEC 68-2-27: Total of 18 shocks 50g, 1-msec, ½ sine, 3-shocks in each direction of 3 orthogonal axes						
Vibration (survival)	Per IEC 68-2-6: 10 cycles in each of 3 orthogonal axes Displacement = 0.75mm-peak, 10Hz to 55Hz Acceleration = 10g-pak, 55Hz to 500Hz						
RoHS	RoHS Full RoHS Compliance						

Mechanical and Pinout:



Data Sheet Revision Table:

Date	Rev.	Orig.	Details of Revision
01/25/13	2	BRM	Added ESD and MSL specifications.
08/13/12	1	BRM	Assigned a model number.
08/02/12		BRM	Original Draft.