XO5166 and XO5167 Series

14 DIP, 3.3 or 5.0 Volt, HCMOS Compatible Output, OCXO







0.32 [8.1]

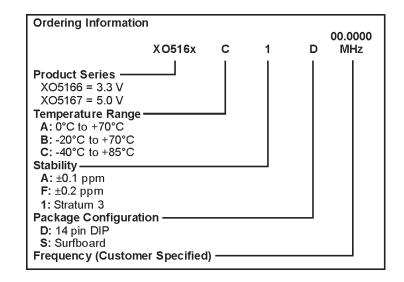
MAX

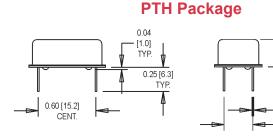
0.02 [0.5] DIA

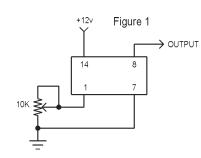
0.30 [7.6] CENT.

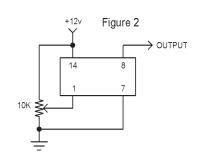
Features:

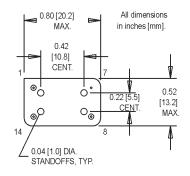
- 3.3 V or 5.0 V Supply
- Wide Frequency Range of 10 to 100 MHz
- Vectron EX-380 Alternative
- RoHS Compliant
- Full Stratum 3 Compliance (including short term hold-over stability)
- SMT Surfboard Option







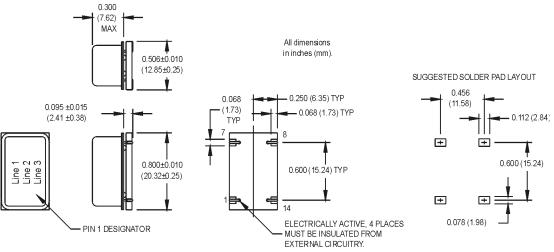




Pin Connections

PIN	FUNCTION
1	Frequency Adjust
7	Case ground & supply return
8	R.F. Output
14	Supply (+)

SMTPackage



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XO5166 and XO5167 Series









	PARAMETER	Symbo	ol .	Min.	Тур.	Max.	Units	Condition	
	Frequency Range	Fo		10		100	MHz		
	Operating Temperature	T _A		(See ordering information)			°C		
	Frequency Stability			(See ordering information)					
	Stratum 3 Free Run Stability					±4.6	ppm	All causes for 20 years	
	Stratum 3 Holdover Stability					±0.280	ppm	For 24 hours (temperature only)	
	Short-Term Stability					5 x 10 ⁻¹⁰		Tau = 0.1 to 30 seconds	
	Frequency vs. Supply					2 x 10 ⁻⁸		Per percentage of voltage change	
	Frequency vs. Aging					7 x 10 ⁻⁷		First year	
	Frequency vs. Load					±0.01	ppm		
	Supply Voltage Vdd			3.15	3.3	3.45	V	XO5166 only	
		Vdd		4.75	5	5.25	V	XO5167 only	
Specifications	Supply Current Idd					110	mA	3.3 VDC at +30°C (XO5166)	
atic		Idd				160	mA	3.3 VDC at -20°C (XO5166)	
ίς		Idd				70	mA	5.0 VDC at +30°C (XO5167)	
i) e		Idd				110	mA	5.0 VDC at -20°C (XO5167)	
Sp	Turn-On Current					250	mΑ	3.3 VDC (XO5166)	
						250	mA	5.0 VDC (XO5167)	
Electrical	Warm-Up Time					±0.1	ppm	2 min after power up following 24 hour off time – reference to frequency after 1 hour of operation	
	Tuning Voltage	V _T		0	1.65	3.3	V	XO5166 (See Figure 1)	
				0.5	2.25	5.0	V	XO5167 (See Figure 1)	
	Frequency Adjustment			±4.0			ppm	Over tuning voltage range	
	Output Logic Type			HCMOS Compatible		patible			
	Symmetry	Sym		45	50	55	%	Ref. To ½ Vdd	
	Output Load					15	рF		
	ise/Fall Time (10% to 90%) Tr/Tf					6	nS	1-50 MHz	
		Tr/Tf				3	nS	>50 MHz	
	Logic Level "0"	V_{OL}				0.4	V		
	Logic Level "1"	V_{OH}		Vdd -0.5			V		
	Phase Noise (Typical)	1 Hz	10 Hz	100 Hz	1kHz	1 kHz	10 kHz	Units	
	10 MHz	-70	-100	-130	-140	-145	-150	dBc/Hz	
	80 MHz	-50	-80	-110	-130	-140	-140	dBc/Hz	
	Mechanical Shock	2000 g, 0.3 mS, ½ sine							
Environmental	Vibration	20 Hz – 2 kHz, 10 g max							
ner	Storage Temperature	-55°C to +125°C							
اڃا	Hermeticity	Per MIL-STD-202, Method 112							
١٤١	Solderability	Per EIAJ-STD-002							
ايًا	Max Soldering Conditions	+245°C for 10 seconds max (DIP version only)							
	Max Soldering Conditions	+220°C	for 10 s	econds max	(SMT ver	sion only)			
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HCMOS Load – see load circuit diagram #2.

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