

Specification for a Stratum 3E LVCMOS SMT OCXO

MtronPTI P/N: XO5184-015sR

Electrical Specifications:

Parameter	Symbol	Min.	Typ.	Max.	Units	Conditions
Nominal Frequency	F _o		12.800000		MHz	
Initial Accuracy	F _i	-100		+100	ppb	@ 25°C at time of shipment
Frequency Stabilities						
Overall		-4.6		+4.6	ppm	
vs. Temperature	ΔF _T /F			10	ppb	Over operating temperature range
vs. Supply Voltage	ΔF _{VDD} /F	-3		+3	ppb	5% change in voltage
Short Term Stability			1 x 10 ⁻¹¹		per sec.	Allan Variance <i>Tau</i> = 1 sec.
Holdover/Day		-1		+1	ppb	Constant voltage and load; <±3°C change in 24 hours
1 Month Aging		-30		+30	ppb	After 30 days continuous operation @ 25°C
1 Year Aging		-100		+100	ppb	After 30 days continuous operation @ 25°C
20 Year Aging		-2.0		+2.0	ppm	After 30 days continuous operation @ 25°C
RF Output						
Output Type		LVCMOS				
Output Load			15		pF	+/- 5% change
Symmetry (duty cycle)	T _{DC}	40	50	60	%	@ 50% of waveform
Rise/Fall Time	T _R /T _F			10	ns	From 10% to 90% V _{OUT}
Logic "1" Level	V _{OH}	90% V _{DD}			V	LVCMOS Load
Logic "0" Level	V _{OL}			10% V _{DD}	V	LVCMOS Load
Temperature and Supply Voltage						
Operating Temperature	T _A	-40		+85	°C	
Storage Temperature	T _S	-50		+105	°C	
Operating Voltage	V _{DD}	3.135	3.3	3.465	V	
Power Consumption				1.3	Watts	Steady state @ 25°C In still air
				3.3	Watts	@ warm-up
Warm-up Time (Restabilization)				5	Minutes	Time to be within ±0.1 ppm of the frequency after 1 hour of operation @ 25°C
Additional Parameters						
Phase Noise (typical)			-80		dBc/Hz	1 Hz
			-110		dBc/Hz	10 Hz
			-130		dBc/Hz	100 Hz
			-145		dBc/Hz	1 kHz
			-150		dBc/Hz	10 kHz

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Environmental Conditions:

Solderability	Per EIAJ-STD-002
Soldering Conditions	See figure 1
RoHS	Full RoHS 6 Compliance

Mechanical, Marking and Layout Information:

Part Marking	
Line 1	MtronPTI
Line 2	XO5184-015sR
Line 3	12.80MHz
Line 4	Serial Number
Line 5	Date Code

Legend	
yy	Year
ww	Work Week

Pin	Function
1	N/C
2	N/C
3	Supply Voltage
4	RF Output
5	N/C
6	N/C
7	Case Ground

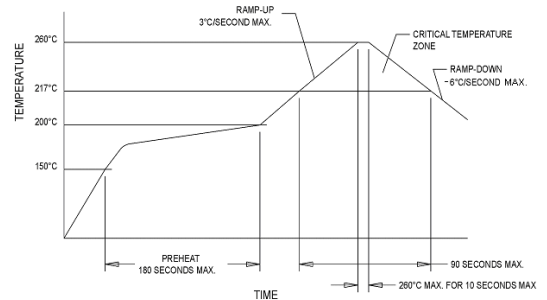
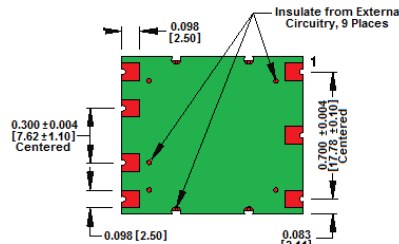
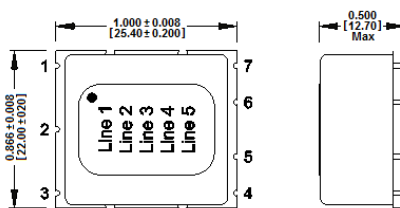
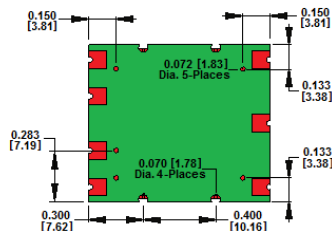
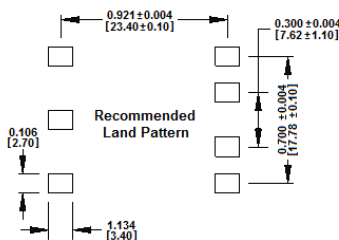


Figure 1



All Dimensions are in Inches [mm]
PAD Numbers are Shown for Reference Only
Tolerances Unless Otherwise Cited are
.xx = ± .01
.xxx = ± .005

■ Exposed Metalization
■ Solder Mask

Data Sheet Revision Table:

Date	Rev.	Orig.	Details of Revision
07-01-14	G	BRM	Removed hermeticity specification point.
12-17-13	F	BRM	Removed redundant units (per sec) in the Units filed for Short Term Stability.
11-07-13	E	BRM	Updated the Outline Drawings for Improved Detail and Clarity at the customer's request.
08-24-13	D	MM	Updated Frequency vs Stability Specification.
08-13-12	C	MM	Updated Short Term Stability Specification.
05-14-12	B	MM	Updated Mechanical Drawing.
02-13-12	A	MM	Added Cisco part number.
07-08-11	0	MM	Original Release.